

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
1	IMPACT - Dave Curtis (UCB) & Lil Reichenenthal (GSFC)	39%	443 d	2/26/03	11/26/04	0 d		
2	Suite Deliveries to APL	0%	25 d	8/31/04	10/6/04	15 d		
3	Boom Suite FM 1 Ready for Delivery to APL	0%	0 d	9/13/04	9/13/04	18 d	358	
4	Boom Suite FM 2 Ready for Delivery to APL	0%	0 d	8/31/04	8/31/04	40 d	365	
5	SEP Suite FM 1 Ready for Delivery to APL	0%	0 d	9/30/04	9/30/04	5 d	863,678,607	
6	SEP Suite FM 2 Ready for Delivery to APL	0%	0 d	10/6/04	10/6/04	15 d	871,681,610	
7	Environmental Testing - PER, Impact Suite EMC, Snapshots of Boom & SEP Suite T	0%	187 d	1/14/04	10/6/04	34 d		
8	Reviews (PER and TRR's) - TRR Dates are Tentative	14%	137 d	1/14/04	7/27/04	84 d		
9	Instrument PER at UCB	100%	1 d	1/14/04	1/14/04	0 d		
10	TRR 1 - Prior to FM 1 SEPT Vibration/Acoustics	0%	1 d	2/2/04	2/2/04	208 d	38SS-5 d	
11	TRR 2 - Prior to FM 1 IDPU Vibration	0%	1 d	3/8/04	3/8/04	184 d	18SS-5 d	
12	TRR 3 - Prior to FM 1 STE-U Thermal Vac/Thermal Balance	0%	1 d	3/10/04	3/10/04	182 d	21SS-5 d	
13	TRR 4 - Prior to FM 1 Boom Structure Vibration	0%	1 d	4/8/04	4/8/04	161 d	20SS-5 d	
14	TRR 5 - Prior to FM 1 SWEA Vibration	0%	1 d	5/3/04	5/3/04	144 d	23SS-5 d	
15	TRR 6 - Prior to FM 2 SEP Suite EMC	0%	1 d	7/27/04	7/27/04	84 d	55SS-5 d	
16	IMPACT Suite FM 1 EMC Testing at UCB	0%	10 d	7/20/04	8/2/04	10 d	839,356	858FS+3 d,677,179,3-
17	Boom Suite FM 1 Environmental Test Flow Snapshot - UCB	0%	119 d	3/15/04	8/27/04	61 d		
18	IDPU Vibration	0%	5 d	3/15/04	3/19/04	175 d	177SS	11SS-5 d
19	IDPU Thermal Vac	0%	10 d	3/22/04	4/2/04	165 d	178SS	
20	Boom Structure Vibration	0%	4 d	4/15/04	4/20/04	153 d	216SS	13SS-5 d
21	STE-U Thermal Vac and Thermal Balance	0%	14 d	3/17/04	4/5/04	164 d	276SS	12SS-5 d
22	Boom Structure Thermal Vac	0%	30 d	4/21/04	6/2/04	123 d	217SS	
23	SWEA/STE Vibration	0%	5 d	5/10/04	5/14/04	135 d	342SS	14SS-5 d
24	SWEA/STE Thermal Vac and Thermal Balance	0%	14 d	8/3/04	8/20/04	66 d	343SS	
25	IDPU Bake Out	0%	5 d	8/3/04	8/9/04	75 d	179SS	
26	Boom Suite Bake Out	0%	5 d	8/23/04	8/27/04	61 d	357SS	
27	Boom Suite FM 2 Environmental Test Flow Snapshot - UCB	0%	87 d	4/16/04	8/17/04	69 d		
28	Boom Structure Vibration	0%	4 d	4/16/04	4/21/04	152 d	238SS	
29	IDPU Vibration	0%	5 d	5/10/04	5/14/04	135 d	182SS	
30	IDPU Thermal Vac	0%	10 d	5/17/04	5/28/04	125 d	183SS	
31	Boom Structure Thermal Vac	0%	30 d	5/12/04	6/23/04	108 d	239SS	
32	SWEA/STE Vibration	0%	5 d	6/23/04	6/29/04	104 d	350SS	
33	SWEA/STE Thermal Vac	0%	10 d	6/30/04	7/13/04	94 d	351SS	
34	Boom Suite FM 2 EMC	0%	5 d	8/4/04	8/10/04	74 d	363SS	
35	IDPU Bake Out	0%	5 d	8/11/04	8/17/04	69 d	184SS	
36	Boom Suite Bake Out	0%	5 d	8/11/04	8/17/04	69 d	364SS	
37	SEP Suite FM 1 Environmental Test Flow Snapshot	0%	162 d	2/9/04	9/24/04	42 d		
38	SEPT Vibe and Acoustics (in Europe)	0%	10 d	2/9/04	2/23/04	194 d	648SS,655SS	10SS-5 d
39	SEPT Thermal Vac (in Europe)	0%	10 d	2/24/04	3/8/04	184 d	649SS,656SS	
40	SEPT Bake Out (in Europe)	0%	3 d	3/9/04	3/11/04	181 d	650SS,657SS	

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41	SEPT Thermal Balance (at GSFC)	0%	10 d	8/3/04	8/16/04	70 d	651SS,658SS	
42	LET/HET/SEP Cen Vibe and Acoustics (at JPL)	0%	10 d	8/6/04	8/19/04	67 d	858SS	
43	LET/HET/SEP Cen Thermal Balance (at JPL)	0%	5 d	8/20/04	8/26/04	62 d	859SS	
44	LET/HET/SEP Cen Thermal Vac (at JPL)	0%	15 d	8/27/04	9/17/04	47 d	860SS	
45	LET/HET/SEP Cen Bake Out (at JPL)	0%	3 d	9/20/04	9/22/04	44 d	863SS	
46	LET/HET/SEP Cen Accelerator Test (at MSU)	0%	10 d	7/15/04	7/28/04	83 d	873SS	
47	SIT Vibe and Acoustics (at GSFC)	0%	10 d	8/12/04	8/25/04	63 d	799SS	
48	SIT Thermal Balance (at GSFC)	0%	5 d	8/27/04	9/2/04	57 d	801SS	
49	SIT Thermal Vac (at GSFC)	0%	10 d	9/3/04	9/17/04	47 d	802SS	
50	SIT Bake Out (at GSFC)	0%	3 d	9/22/04	9/24/04	42 d	804SS	
51	SEP Suite FM 2 Environmental Test Flow Snapshot	0%	174 d	2/3/04	10/6/04	34 d		
52	SEPT Vibe and Acoustics (in Europe)	0%	10 d	2/3/04	2/17/04	198 d	663SS,670SS	
53	SEPT Thermal Vac (in Europe)	0%	10 d	2/18/04	3/2/04	188 d	664SS,671SS	
54	SEPT Bake Out (in Europe)	0%	3 d	3/3/04	3/5/04	185 d	665SS,672SS	
55	SEP Suite FM 2 EMC (at Caltech)	0%	5 d	8/3/04	8/9/04	75 d	865SS	15SS-5 d
56	SEPT Thermal Balance (at GSFC)	0%	10 d	8/3/04	8/16/04	70 d	666SS,673SS	
57	LET/HET/SEP Cen Vibe and Acoustics (at JPL)	0%	10 d	8/20/04	9/2/04	57 d	866SS	
58	LET/HET/SEP Cen Thermal Balance (at JPL)	0%	5 d	9/3/04	9/10/04	52 d	867SS	
59	LET/HET/SEP Cen Thermal Vac (at JPL)	0%	15 d	9/13/04	10/1/04	37 d	868SS	
60	LET/HET/SEP Cen Bake Out (at JPL)	0%	3 d	10/4/04	10/6/04	34 d	871SS	
61	SIT Vibe and Acoustics (at GSFC)	0%	10 d	8/12/04	8/25/04	63 d	799SS	
62	SIT Thermal Balance (at GSFC)	0%	5 d	8/27/04	9/2/04	57 d	801SS	
63	SIT Thermal Vac (at GSFC)	0%	10 d	9/3/04	9/17/04	47 d	802SS	
64	SIT Bake Out (at GSFC)	0%	3 d	9/22/04	9/24/04	42 d	804SS	
65	Power Converters - UCB	44%	355 d	2/26/03	7/21/04	88 d		
66	Deliveries	20%	100 d	12/9/03	4/30/04	145 d		
67	FM 1	40%	70 d	12/9/03	3/19/04	175 d		
68	Deliver IDPU FM 1 LVPS (stays at UCB)	0%	0 d	3/5/04	3/5/04	74 d	122	176
69	Deliver SEP FM 1 LVPS (to Cal Tech)	0%	1 d	3/17/04	3/17/04	62 d	126	828
70	Deliver SWEA/STE FM 1 LVPS (stays at UCB)	0%	0 d	3/19/04	3/19/04	49 d	130	338
71	Deliver PLASTIC FM 1 LVPS (to UNH)	0%	2 d	3/4/04	3/5/04	185 d	134	
72	Deliver SIT FM 1 HVPS (to UMD)	100%	2 d	12/9/03	12/10/03	0 d	136	762
73	FM 2	0%	28 d	3/24/04	4/30/04	34 d		
74	Deliver IDPU FM 2 LVPS (stays at UCB)	0%	0 d	4/30/04	4/30/04	40 d	143	181
75	Deliver SEP FM 2 LVPS (to Cal Tech)	0%	1 d	4/19/04	4/19/04	77 d	147	852
76	Deliver SWEA/STE FM 2 LVPS (stays at UCB)	0%	0 d	4/26/04	4/26/04	49 d	151	347
77	Deliver PLASTIC FM 2 LVPS (to UNH)	0%	2 d	4/12/04	4/13/04	158 d	155	
78	Deliver SIT FM 2 HVPS (to UMD)	0%	2 d	3/24/04	3/25/04	34 d	159	764
79	Remaining Flight Procurements	75%	44 d	10/24/03	12/29/03	74 d		
80	Procure IDPU Flight Printed Wiring Board	0%	10 d	12/15/03	12/29/03	74 d	86	114

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81	✓	Procure SEP Flight Printed Wiring Board	100%	10 d	11/24/03	12/8/03	0 d	87	115
82	✓	Procure SWEA Flight Printed Wiring Board	100%	10 d	12/4/03	12/17/03	0 d	88	116
83	✓	Procure PLASTIC Flight Printed Wiring Board	100%	10 d	10/24/03	11/6/03	0 d		117
84		Fabrication	86%	226 d	2/26/03	1/20/04	171 d		
85	✓	FM 1 & FM 2	100%	195 d	2/26/03	12/3/03	0 d		
86	✓	Re-layout IDPU LVPS Flight Printed Wiring Board	100%	186 d	2/26/03	11/19/03	0 d		80
87	✓	Re-layout SEP LVPS Flight Printed Wiring Board	100%	32 d	9/24/03	11/7/03	0 d		81
88	✓	Re-layout SWEA LVPS Flight Printed Wiring Board	100%	5 d	11/26/03	12/3/03	0 d		82
89	✓	FM 1	100%	45 d	9/29/03	12/3/03	0 d		
90	✓	Wind IDPU LVPS FM 1 Magnets	100%	10 d	10/17/03	10/30/03	0 d		102
91	✓	Wind SEP LVPS FM 1 Magnets	100%	15 d	9/29/03	10/20/03	0 d		103
92	✓	Wind SWEA LVPS FM 1 Magnets	100%	5 d	11/26/03	12/3/03	0 d		104
93	✓	Wind PLASTIC LVPS FM 1 Magnets	100%	45 d	9/29/03	12/3/03	0 d		105
94		FM 2	30%	15 d	12/29/03	1/20/04	171 d		
95	☒	Wind HVPS FM 2 Magnets	30%	15 d	12/29/03	1/20/04	34 d		111,96SS,97SS,98SS
96		Wind IDPU LVPS FM 2 Magnets	30%	15 d	12/29/03	1/20/04	67 d	95SS	107
97		Wind SEP LVPS FM 2 Magnets	30%	15 d	12/29/03	1/20/04	94 d	95SS	108
98		Wind SWEA LVPS FM 2 Magnets	30%	15 d	12/29/03	1/20/04	72 d	95SS	109
99		Wind PLASTIC LVPS FM 2 Magnets	30%	15 d	12/29/03	1/20/04	171 d	95SS	110
100		Assembly	2%	57 d	12/29/03	3/19/04	143 d		
101		FM 1	5%	27 d	12/29/03	2/5/04	173 d		
102		Assemble IDPU FM 1 LVPS	0%	12 d	1/6/04	1/22/04	74 d	90,114	120
103	☒	Assemble SEP FM 1 LVPS	0%	15 d	1/12/04	2/2/04	62 d	115,91	124
104	☒	Assemble SWEA FM 1 LVPS	0%	14 d	1/16/04	2/5/04	49 d	92,116	128
105	☒	Assemble PLASTIC FM 1 LVPS	20%	15 d	12/29/03	1/20/04	185 d	117,93	132
106		FM 2	0%	42 d	1/21/04	3/19/04	34 d		
107	☒	Assemble IDPU FM 2 LVPS	0%	15 d	3/1/04	3/19/04	40 d	96	141
108	☒	Assemble SEP FM 2 LVPS	0%	15 d	2/13/04	3/5/04	77 d	97	145
109	☒	Assemble SWEA FM 2 LVPS	0%	15 d	2/24/04	3/15/04	49 d	98	149
110	☒	Assemble PLASTIC FM 2 LVPS	0%	14 d	2/9/04	2/27/04	158 d	99	153
111		Assemble SIT FM 2 HVPS	0%	14 d	1/21/04	2/9/04	34 d	95	157
112		Testing	11%	182 d	10/31/03	7/21/04	88 d		
113		FM 1 & FM 2	53%	36 d	11/24/03	1/15/04	66 d		
114		Coupon Test IDPU Flight Printed Wiring Board	0%	4 d	12/30/03	1/5/04	74 d	80	102
115	✓	Coupon Test SEP Flight Printed Wiring Board	100%	5 d	12/9/03	12/15/03	0 d	81	103
116	☒	Coupon Test SWEA Flight Printed Wiring Board	0%	5 d	1/9/04	1/15/04	49 d	82	104
117	✓	Coupon Test PLASTIC Flight Printed Wiring Board	100%	5 d	11/24/03	12/1/03	0 d	83	105
118		FM 1	16%	182 d	10/31/03	7/21/04	88 d		
119		IDPU	0%	30 d	1/23/04	3/5/04	74 d		
120		Test IDPU FM 1 LVPS	0%	15 d	1/23/04	2/12/04	74 d	102	121

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121	Coat & Stake IDPU LVPS FM 1 Boards	0%	5 d	2/13/04	2/20/04	74 d	120	122
122	Retest IDPU LVPS FM 1 Boards	0%	10 d	2/23/04	3/5/04	74 d	121	68
123	SEP	0%	30 d	2/3/04	3/16/04	62 d		
124	Test SEP FM 1 LVPS	0%	15 d	2/3/04	2/24/04	62 d	103	125
125	Coat & Stake SEP LVPS FM 1 Boards	0%	5 d	2/25/04	3/2/04	62 d	124	126
126	Retest SEP LVPS FM 1 Boards	0%	10 d	3/3/04	3/16/04	62 d	125,377	69
127	SWEA	0%	30 d	2/6/04	3/19/04	49 d		
128	Test SWEA FM 1 LVPS	0%	15 d	2/6/04	2/27/04	49 d	104	129
129	Coat & Stake SWEA LVPS FM 1 Boards	0%	5 d	3/1/04	3/5/04	49 d	128	130
130	Retest SWEA LVPS FM 1 Boards	0%	10 d	3/8/04	3/19/04	49 d	129	70
131	PLASTIC	0%	30 d	1/21/04	3/3/04	185 d		
132	Test PLASTIC FM 1 LVPS	0%	15 d	1/21/04	2/10/04	185 d	105	133
133	Coat & Stake PLASTIC LVPS FM 1 Boards	0%	5 d	2/11/04	2/18/04	185 d	132	134
134	Retest PLASTIC LVPS FM 1 Boards	0%	10 d	2/19/04	3/3/04	185 d	133	71
135	SIT	63%	182 d	10/31/03	7/21/04	88 d		
136	Test SIT FM 1 HVPS	100%	25 d	10/31/03	12/8/03	0 d		72
137	Coat & Stake SIT FM 1 HVPS Boards (at UMD)	0%	5 d	7/1/04	7/7/04	88 d	772SS	138
138	Retest SIT FM 1 HVPS Boards (at UMD)	0%	10 d	7/8/04	7/21/04	88 d	137	
139	FM 2	0%	58 d	2/10/04	4/30/04	34 d		
140	IDPU	0%	30 d	3/22/04	4/30/04	40 d		
141	Test IDPU FM 2 LVPS	0%	15 d	3/22/04	4/9/04	40 d	107	142
142	Coat & Stake IDPU LVPS FM 2 Boards	0%	5 d	4/12/04	4/16/04	40 d	141	143
143	Retest IDPU LVPS FM 2 Boards	0%	10 d	4/19/04	4/30/04	40 d	142	74
144	SEP	0%	30 d	3/8/04	4/16/04	77 d		
145	Test SEP FM 2 LVPS	0%	15 d	3/8/04	3/26/04	77 d	108	146
146	Coat & Stake SEP LVPS FM 2 Boards	0%	5 d	3/29/04	4/2/04	77 d	145	147
147	Retest SEP LVPS FM 2 Boards	0%	10 d	4/5/04	4/16/04	77 d	146,377	75
148	SWEA	0%	30 d	3/16/04	4/26/04	49 d		
149	Test SWEA FM 2 LVPS	0%	15 d	3/16/04	4/5/04	49 d	109	150
150	Coat & Stake SWEA LVPS FM 2 Boards	0%	5 d	4/6/04	4/12/04	49 d	149	151
151	Retest SWEA LVPS FM 2 Boards	0%	10 d	4/13/04	4/26/04	49 d	150	76
152	PLASTIC	0%	30 d	3/1/04	4/9/04	158 d		
153	Test PLASTIC FM 2 LVPS	0%	15 d	3/1/04	3/19/04	158 d	110	154
154	Coat & Stake PLASTIC LVPS FM 2 Boards	0%	5 d	3/22/04	3/26/04	158 d	153	155
155	Retest PLASTIC LVPS FM 2 Boards	0%	10 d	3/29/04	4/9/04	158 d	154	77
156	SIT	0%	30 d	2/10/04	3/23/04	34 d		
157	Test SIT FM 2 HVPS	0%	15 d	2/10/04	3/2/04	34 d	111	158
158	Coat & Stake SIT FM 2 HVPS Boards	0%	5 d	3/3/04	3/9/04	34 d	157	159
159	Retest SIT FM 2 HVPS Boards	0%	10 d	3/10/04	3/23/04	34 d	158	78
160	IDPU - UCB	59%	340 d	5/5/03	9/7/04	55 d		

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161		Deliveries	0%	78 d	2/9/04	5/28/04	112 d		
162		IDPU FM 1 Complete (to CIT for SEP I&T, then to UCB for Boom I&T, Suite EM)	0%	0 d	4/2/04	4/2/04	74 d	178	834
163		IDPU FM 2 Complete (to CIT for SEP I&T, then to UCB for Boom I&T, Boom EM)	0%	0 d	5/28/04	5/28/04	40 d	183	847
164		Deliver IDPU S/W Build 3 (to UNH)	0%	0 d	2/9/04	2/9/04	203 d	167	
165		Flight Software	81%	192 d	5/5/03	2/9/04	108 d		
166	✓	IDPU S/W Build #2.5	100%	131 d	5/5/03	11/7/03	0 d		176
167	☒	IDPU S/W Build #3	60%	120 d	8/15/03	2/9/04	108 d		183,164
168		GSE	50%	301 d	6/30/03	9/7/04	55 d		
169	✓	STE Science Display	100%	27 d	10/23/03	12/2/03	0 d		
170	☒	SWEA Science Display	62%	140 d	6/30/03	1/21/04	216 d		
171	☒	GSE Continued Development	40%	291 d	7/15/03	9/7/04	55 d		
172		Integration & Test	33%	218 d	10/7/03	8/17/04	40 d		
173	✓	IDPU DCB FM 2 Board Level Testing	100%	15 d	10/7/03	10/28/03	0 d		181,174
174	✓	IDPU DCB Clean/Stake/Coat (FM 1 & FM 2)	100%	10 d	12/26/03	1/9/04	0 d	173	176,181
175		FM 1	0%	110 d	3/8/04	8/9/04	32 d		
176		IDPU FM 1 I & T	0%	5 d	3/8/04	3/12/04	74 d	166,68,243,287,17	177
177		IDPU FM 1 Vibration	0%	5 d	3/15/04	3/19/04	74 d	176	178,18SS
178		IDPU FM 1 Thermal Vacuum	0%	10 d	3/22/04	4/2/04	74 d	177	162,179,19SS
179		IDPU FM 1 Bakeout	0%	5 d	8/3/04	8/9/04	32 d	16,178	358,25SS
180		FM 2	0%	76 d	5/3/04	8/17/04	40 d		
181		IDPU FM 2 I & T	0%	5 d	5/3/04	5/7/04	40 d	74,244,288,173,17	182
182		IDPU FM 2 Vibration	0%	5 d	5/10/04	5/14/04	40 d	181	183,29SS
183		IDPU FM 2 Thermal Vacuum	0%	10 d	5/17/04	5/28/04	40 d	167,182	163,184,30SS
184		IDPU FM 2 Bakeout	0%	5 d	8/11/04	8/17/04	40 d	183,364SS	365,35SS
185		BOOM Suite - UCB	56%	304 d	7/1/03	9/13/04	32 d		
186		BOOM Structure - UCB	59%	187 d	9/10/03	6/8/04	74 d		
187		Deliveries	0%	104 d	1/9/04	6/8/04	74 d		
188	✓	Protoflight Boom available for I&T (if needed)	100%	0 d	1/9/04	1/9/04	0 d	193	
189		Deliver Boom Structure FM 1 (stays at UCB for Boom Suite FM 1 I&T)	0%	0 d	5/17/04	5/17/04	48 d	218	356,355
190		Deliver Boom Structure FM 2 (stays at UCB for Boom Suite FM 2 I&T)	0%	0 d	6/8/04	6/8/04	74 d	240	362,361
191	✓	Protoflight	100%	79 d	9/16/03	1/9/04	0 d		
192	✓	Protoflight Boom Post Environmental Deployment Test	100%	35 d	9/16/03	11/4/03	0 d		193
193	✓	Document Protoflight Boom Test Results	100%	44 d	11/5/03	1/9/04	0 d	192	188
194	✓	Flight Procurements	100%	40 d	9/10/03	11/5/03	0 d		
195	✓	Receive and Inspect Flight Boom Structure Tubes (FM 1)	100%	32 d	9/10/03	10/24/03	0 d		200,201FF+47 d
196	✓	Receive and Inspect Flight Boom Structure Tubes (FM 2)	100%	40 d	9/10/03	11/5/03	0 d		222
197		FM 1	46%	159 d	9/29/03	5/17/04	48 d		
198		Telescoping	59%	81 d	10/27/03	2/24/04	50 d		
199		Fabrication, Assembly & Test	59%	81 d	10/27/03	2/24/04	50 d		
200	✓	Machine FM 1 Boom Structure Tubes as necessary	100%	5 d	10/27/03	10/31/03	0 d	195	202

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201	✓	Machine FM 1 Boom Structure Parts (Rings)	100%	38 d	11/7/03	1/5/04	0 d	195FF+47 d	202,223SS
202	☒	Assemble FM 1 Boom Structure Telescoping Assembly	0%	20 d	1/12/04	2/9/04	50 d	200,201	203
203		Test FM 1 Boom Structure Telescoping Assembly	0%	10 d	2/10/04	2/24/04	50 d	202	214
204		Harness	41%	102 d	9/29/03	2/26/04	48 d		
205		Fabrication	59%	72 d	9/29/03	1/13/04	48 d		
206	☒	Machine FM 1 Harness Parts	59%	72 d	9/29/03	1/13/04	48 d		208,228SS
207		Assembly & Test	0%	30 d	1/14/04	2/26/04	48 d		
208		Assemble FM 1 Harness subassembly	0%	20 d	1/14/04	2/11/04	48 d	206	209
209		FM1 Harness Test	0%	10 d	2/12/04	2/26/04	48 d	208	214
210	✓	Mounting	100%	38 d	9/29/03	11/21/03	0 d		
211	✓	FM 1 Boom Structure Mounting Fabrication	100%	28 d	9/29/03	11/6/03	0 d		212,233SS
212	✓	FM 1 Boom Structure Mounting Assembly	100%	10 d	11/7/03	11/21/03	0 d	211	
213		Integration & Test	0%	57 d	2/27/04	5/17/04	48 d		
214		Integrate FM 1 Boom Structure subassemblies	0%	20 d	2/27/04	3/25/04	48 d	203,209	215
215		FM 1 Boom Structure Deployment Test	0%	10 d	3/26/04	4/8/04	48 d	214	216,354
216		FM 1 Boom Structure Vibration Testing Completed	0%	4 d	4/15/04	4/20/04	48 d	215,354	217,20SS
217		FM 1 Boom Structure Thermal Vac Testing Completed	0%	15 d	4/21/04	5/11/04	48 d	216	218,239,22SS
218		FM 1 Boom Structure Post Environmental Deployment Test Complete	0%	4 d	5/12/04	5/17/04	48 d	217	189
219		FM 2	47%	174 d	9/29/03	6/8/04	74 d		
220		Telescoping	62%	73 d	11/6/03	2/24/04	91 d		
221		Fabrication, Assembly & Test	62%	73 d	11/6/03	2/24/04	91 d		
222	✓	Machine FM 2 Boom Structure Tubes as necessary	100%	11 d	11/6/03	11/21/03	0 d	196	224
223	✓	Machine FM 2 Boom Structure Parts (Rings)	100%	38 d	11/7/03	1/5/04	0 d	201SS	224
224	☒	Assemble FM 2 Boom Structure Telescoping Assembly	0%	20 d	1/12/04	2/9/04	91 d	222,223	225
225		Test FM 2 Boom Structure Telescoping Assembly	0%	10 d	2/10/04	2/24/04	91 d	224	236
226		Harness	41%	102 d	9/29/03	2/26/04	89 d		
227		Fabrication	59%	72 d	9/29/03	1/13/04	89 d		
228		Machine FM 2 Harness Parts	59%	72 d	9/29/03	1/13/04	89 d	206SS	230
229		Assembly & Test	0%	30 d	1/14/04	2/26/04	89 d		
230		Assemble FM 2 Harness subassembly	0%	20 d	1/14/04	2/11/04	89 d	228	231
231		FM 2 Harness Test	0%	10 d	2/12/04	2/26/04	89 d	230	236
232	✓	Mounting	100%	38 d	9/29/03	11/21/03	0 d		
233	✓	FM 2 Boom Structure Mountings Fabrication	100%	28 d	9/29/03	11/6/03	0 d	211SS	234
234	✓	FM 2 Boom Structure Mountings Assembly	100%	10 d	11/7/03	11/21/03	0 d	233	
235		Integration & Test	0%	72 d	2/27/04	6/8/04	74 d		
236		Integrate FM 2 Boom Structure subassemblies	0%	20 d	2/27/04	3/25/04	89 d	225,231	237
237		FM 2 Boom Structure Deployment Test	0%	10 d	3/26/04	4/8/04	89 d	236	238,360
238		FM 2 Boom Structure Vibration Test	0%	4 d	4/16/04	4/21/04	88 d	237,360	239,28SS
239		FM 2 Boom Structure Thermal Vac Testing	0%	15 d	5/12/04	6/2/04	74 d	238,217	240,31SS
240		FM 2 Boom Structure Post Environmental Deployment Test	0%	4 d	6/3/04	6/8/04	74 d	239	190

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
241	MAG - GSFC	0%	47 d	1/9/04	3/17/04	72 d		
242	Deliveries	0%	22 d	2/17/04	3/17/04	72 d		
243	Deliver MAG FM 1 (to UCB for IDPU/BOOM Suite FM 1 I&T)	0%	2 d	2/17/04	2/18/04	84 d	247	176,354
244	Deliver MAG FM 2 (to UCB for IDPU/BOOM Suite FM 2 I&T)	0%	2 d	3/16/04	3/17/04	72 d	250	181,360
245	FM 1	0%	25 d	1/9/04	2/13/04	84 d		
246	FM 1 MAG Testing	0%	10 d	1/9/04	1/23/04	84 d		247
247	MAG FM 1 Calibration	0%	15 d	1/26/04	2/13/04	84 d	246	243
248	FM 2	0%	25 d	2/9/04	3/15/04	72 d		
249	FM 2 MAG Testing	0%	10 d	2/9/04	2/23/04	72 d		250
250	MAG FM 2 Calibration	0%	15 d	2/24/04	3/15/04	72 d	249	244
251	SWEA - CESR	6%	79 d	12/8/03	3/31/04	77 d		
252	Deliveries	50%	79 d	12/8/03	3/31/04	77 d		
253	SWEA FM 1 Delivery to UCB	100%	3 d	12/8/03	12/10/03	0 d		340
254	SWEA FM 2 Delivery to UCB	0%	3 d	3/29/04	3/31/04	77 d	257	348
255	FM 2	0%	47 d	1/21/04	3/26/04	77 d		
256	SWEA FM 2 Final Assembly (Block 2)	0%	3 d	1/21/04	1/23/04	77 d		257
257	SWEA FM 2 Calibration	0%	44 d	1/26/04	3/26/04	77 d	256	254
258	STE - UCB	55%	153 d	8/29/03	4/9/04	88 d		
259	Deliveries	0%	8 d	3/30/04	4/9/04	52 d		
260	Deliver STE-D FM 1 (stays at UCB for SWEA/STE-D FM 1 I&T)	0%	0 d	3/30/04	3/30/04	52 d	275	340
261	Deliver STE-D FM 2 (stays at UCB for SWEA/STE-D FM 2 I&T)	0%	0 d	4/9/04	4/9/04	70 d	283	348
262	Deliver STE-U FM 1 (stays at UCB for BOOM Suite FM 1 I&T)	0%	0 d	4/5/04	4/5/04	51 d	276	354
263	Deliver STE-U FM 2 (stays at UCB for BOOM Suite FM 2 I&T)	0%	0 d	4/9/04	4/9/04	88 d	284	360
264	STE-U/D Housing	71%	38 d	11/21/03	1/16/04	100 d		
265	Fabricate STE FM 1 Box	71%	38 d	11/21/03	1/16/04	61 d		274
266	Fabricate STE FM 2 Box	71%	38 d	11/21/03	1/16/04	100 d		282
267	STE-U/D Detector Boards	51%	153 d	8/29/03	4/9/04	88 d		
268	Coupon Test STE FM 1 & FM 2 Detector Printed Wiring Board	100%	17 d	10/10/03	11/4/03	0 d	270,278	271,279
269	FM 1	47%	149 d	8/29/03	4/5/04	51 d		
270	Fabrication STE FM 1 Detector Printed Wiring Board	100%	29 d	8/29/03	10/9/03	0 d		268
271	Load STE FM 1 Detector Printed Wiring Board	78%	49 d	11/5/03	1/16/04	51 d	268	272
272	Test STE FM 1 Detector Printed Wiring Board	0%	10 d	1/20/04	2/2/04	51 d	271	274
273	Integration & Test	0%	44 d	2/3/04	4/5/04	51 d		
274	Integrate & Test STE-U/D FM 1 Detectors W/ FM 1 Housings	0%	20 d	2/3/04	3/2/04	51 d	265,272	282,275
275	Calibrate STE-U/D FM 1	0%	20 d	3/3/04	3/30/04	51 d	274	276FS-10 d,260
276	STE-U FM 1 Thermal Vacuum/Balance	0%	14 d	3/17/04	4/5/04	51 d	275FS-10 d	262,21SS
277	FM 2	48%	153 d	8/29/03	4/9/04	88 d		
278	Fabricate STE FM 2 Detector Printed Wiring Board	100%	29 d	8/29/03	10/9/03	0 d		268
279	Load STE FM 2 Detector Printed Wiring Board	60%	63 d	11/5/03	2/6/04	76 d	268	280
280	Test STE FM 2 Detector Printed Wiring Board	0%	10 d	2/9/04	2/23/04	76 d	279	282






ID		Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
281		Integration & Test	0%	28 d	3/3/04	4/9/04	70 d		
282		Integrate & Test STE-U/D FM 2 Detectors W/ FM 2 Housings	0%	10 d	3/3/04	3/16/04	70 d	266,280,274	283
283		Calibrate STE-U/D FM 2	0%	18 d	3/17/04	4/9/04	70 d	282	284FS-10 d,261
284		STE-U FM 2 Thermal Vacuum	0%	10 d	3/29/04	4/9/04	88 d	283FS-10 d	263
285		STE and SWEA - UCB	67%	289 d	7/1/03	8/20/04	21 d		
286		Deliveries	0%	119 d	1/26/04	7/13/04	49 d		
287		Deliver STE-U FM 1 Interface (stays at UCB for IDPU FM 1 I&T)	0%	0 d	1/26/04	1/26/04	102 d	336	176
288		Deliver STE-U FM 2 Interface (stays at UCB for IDPU FM 2 I&T)	0%	0 d	1/26/04	1/26/04	108 d	345	181
289		Deliver SWEA/STE-D FM 1 (stays at UCB for Boom Suite FM 1 I&T)	0%	0 d	5/14/04	5/14/04	49 d	342	355
290		Deliver SWEA/STE-D FM 2 (stays at UCB for Boom Suite FM 2 I&T)	0%	0 d	7/13/04	7/13/04	49 d	351	361
291		SWEA Pedestal	92%	137 d	7/1/03	1/16/04	98 d		
292		Fabricate SWEA FM 1 Baseplate	92%	137 d	7/1/03	1/16/04	72 d		337
293		Fabricate SWEA FM 2 Baseplate	92%	137 d	7/1/03	1/16/04	98 d		346
294		STE-D Pre-Amp Board	89%	54 d	9/22/03	12/9/03	124 d		
295		FM 1	87%	54 d	9/22/03	12/9/03	98 d		
296	✓	Fabricate STE-D FM 1 Preamp Printed Wiring Board	100%	34 d	9/22/03	11/7/03	0 d		301
297		Load STE-D FM 1 Preamp Printed Wiring Board	0%	5 d	12/3/03	12/9/03	98 d	301	337
298		FM 2	87%	54 d	9/22/03	12/9/03	124 d		
299	✓	Fabricate STE-D FM 2 Preamp Printed Wiring Board	100%	34 d	9/22/03	11/7/03	0 d		301
300		Load STE-D FM 2 Preamp Printed Wiring Board	0%	5 d	12/3/03	12/9/03	124 d	301	346
301	✓	Coupon Test STE-D FM 1 & FM 2 Pre-Amp Printed Wiring Boards	100%	15 d	11/10/03	12/2/03	0 d	296,299	297,300
302		STE-D Shaper Board	64%	30 d	12/4/03	1/16/04	98 d		
303		FM 1	67%	30 d	12/4/03	1/16/04	72 d		
304	✓	Fabricate STE-D FM 1 Shaper Printed Wiring Board	100%	10 d	12/4/03	12/17/03	0 d		309
305		Load STE-D FM 1 Shaper Printed Wiring Board	0%	5 d	1/12/04	1/16/04	72 d	309	337
306		FM 2	67%	30 d	12/4/03	1/16/04	98 d		
307	✓	Fabricate STE-D FM 2 Shaper Printed Wiring Board	100%	10 d	12/4/03	12/17/03	0 d		309
308		Load STE-D FM 2 Shaper Printed Wiring Board	0%	5 d	1/12/04	1/16/04	98 d	309	346
309		Coupon Test STE-D FM 1 & FM 2 Shaper Printed Wiring Boards	60%	15 d	12/18/03	1/9/04	72 d	304,307	305,308
310		STE-D Interface Board	64%	30 d	12/4/03	1/16/04	98 d		
311		FM 1	67%	30 d	12/4/03	1/16/04	72 d		
312	✓	Fabricate SWEA / STE-D FM 1 Digital to Analog Converter Printed Wir	100%	10 d	12/4/03	12/17/03	0 d		317
313		Load SWEA / STE-D FM 1 Digital to Analog Converter Printed Wiring I	0%	5 d	1/12/04	1/16/04	72 d	317	337
314		FM 2	67%	30 d	12/4/03	1/16/04	98 d		
315	✓	Fabricate SWEA / STE-D FM 2 Digital to Analog Converter Printed Wir	100%	10 d	12/4/03	12/17/03	0 d		317
316		Load SWEA / STE-D FM 2 Digital to Analog Converter Printed Wiring I	0%	5 d	1/12/04	1/16/04	98 d	317	346
317		Coupon Test STE-D FM 1 & FM 2 D/A Converter Printed Wiring Boards	60%	15 d	12/18/03	1/9/04	72 d	312,315	313,316
318		STE-U Pre-Amp Board	95%	79 d	9/22/03	1/15/04	109 d		
319	✓	FM 1	100%	79 d	9/22/03	1/15/04	0 d		
320	✓	Fabricate STE-U FM 1 Preamp Printed Wiring Board	100%	34 d	9/22/03	11/7/03	0 d		325

ID		Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
321	✓	Load STE-U FM 1 Preamp Printed Wiring Board	100%	4 d	1/12/04	1/15/04	0 d	325	336
322		FM 2	87%	54 d	9/22/03	12/9/03	134 d		
323	✓	Fabricate STE-U FM 2 Preamp Printed Wiring Board	100%	34 d	9/22/03	11/7/03	0 d		325
324		Load STE-U FM 2 Preamp Printed Wiring Board	0%	5 d	12/3/03	12/9/03	134 d	325	345
325	✓	Coupon Test STE-U FM 1 & FM 2 Pre-Amp Printed Wiring Boards	100%	15 d	11/10/03	12/2/03	0 d	320,323	321,324
326		STE-U Shaper & Interface Board	64%	30 d	12/4/03	1/16/04	108 d		
327		FM 1	67%	30 d	12/4/03	1/16/04	102 d		
328	✓	Fabricate STE-U FM 1 Shaper & Interface Printed Wiring Board	100%	10 d	12/4/03	12/17/03	0 d		333
329		Load STE-U FM 1 Shaper & Interface Printed Wiring Board	0%	5 d	1/12/04	1/16/04	102 d	333	336
330		FM 2	67%	30 d	12/4/03	1/16/04	108 d		
331	✓	Fabricate STE-U FM 2 Shaper & Interface Printed Wiring Board	100%	10 d	12/4/03	12/17/03	0 d		333
332		Load STE-U FM 2 Shaper & Interface Printed Wiring Board	0%	5 d	1/12/04	1/16/04	108 d	333	345
333		Coupon Test STE-U FM 1 & FM 2 Shaper & Interface Printed Wiring Board	60%	15 d	12/18/03	1/9/04	102 d	328,331	329,332
334		Integration & Test	0%	152 d	1/20/04	8/20/04	21 d		
335		FM 1	0%	152 d	1/20/04	8/20/04	18 d		
336		Integrate & Test STE-U FM 1 Interface Boards	0%	5 d	1/20/04	1/26/04	102 d	321,329	287
337		Integrate & Test SWEA/STE-D FM 1 Interface Boards	0%	20 d	1/20/04	2/17/04	72 d	292,297,305,313	338,339
338		Integrate & Test SWEA/STE-D FM 1 Interface with SWEA/STE-D FM	0%	10 d	3/22/04	4/2/04	49 d	337,70	340
339		Clean/Coat/Stake SWEA/STE-D Interface	0%	10 d	2/18/04	3/2/04	72 d	337	340
340		Integrate & Test SWEA/STE-D FM 1 Combined	0%	5 d	4/5/04	4/9/04	49 d	338,339,253,260	341
341		Calibrate SWEA/STE FM 1	0%	20 d	4/12/04	5/7/04	49 d	340	342
342		SWEA/STE FM 1 Vibration	0%	5 d	5/10/04	5/14/04	49 d	341	289,23SS
343		SWEA/STE-D FM 1 Thermal Vac/Thermal Balance	0%	14 d	8/3/04	8/20/04	18 d	16	357,24SS
344		FM 2	0%	124 d	1/20/04	7/13/04	49 d		
345		Integrate & Test STE-U FM 2 Interface Boards	0%	5 d	1/20/04	1/26/04	108 d	324,332	288
346		Integrate & Test SWEA/STE-D FM 2 Interface Boards	0%	20 d	1/20/04	2/17/04	98 d	293,300,308,316	347
347		Integrate & Test SWEA/STE-D FM 2 Interface with SWEA/STE-D FM	0%	10 d	4/27/04	5/10/04	49 d	346,76	348
348		Integrate & Test SWEA/STE-D FM 2 Combined	0%	10 d	5/11/04	5/24/04	49 d	347,254,261	349
349		Calibrate SWEA/STE FM 2	0%	20 d	5/25/04	6/22/04	49 d	348	350
350		SWEA/STE FM 2 Vibration	0%	5 d	6/23/04	6/29/04	49 d	349	351,32SS
351		SWEA/STE-D FM 2 Thermal Vac	0%	10 d	6/30/04	7/13/04	49 d	350	290,33SS
352		Boom Suite I&T - UCB	0%	110 d	4/9/04	9/13/04	32 d		
353		FM 1	0%	110 d	4/9/04	9/13/04	18 d		
354		FM 1 Boom/MAG/STE-U I&T	0%	4 d	4/9/04	4/14/04	48 d	215,243,262	216
355		FM 1 Boom/SWEA/STE/MAG I&T	0%	2 d	5/18/04	5/19/04	48 d	289,189	356
356		FM 1 Integrated Boom/IDPU I&T	0%	4 d	7/1/04	7/6/04	19 d	355,189,836	357,16
357		BOOM Suite FM 1 Bakeout	0%	5 d	8/23/04	8/27/04	18 d	356,343	358,26SS
358		Final BOOM Suite FM 1 I & T	0%	10 d	8/30/04	9/13/04	18 d	357,179	3
359		FM 2	0%	101 d	4/12/04	8/31/04	40 d		
360		FM 2 Boom/MAG/STE-U I&T	0%	4 d	4/12/04	4/15/04	88 d	237,244,263	238







ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
361	FM 2 Boom/SWEA/STE/MAG I&T	0%	2 d	7/14/04	7/15/04	49 d	290,190	362
362	FM 2 Integrated Boom/IDPU I&T	0%	4 d	7/29/04	8/3/04	40 d	361,849,190	363
363	Boom Suite FM 2 EMC	0%	5 d	8/4/04	8/10/04	40 d	362	364,34SS
364	BOOM Suite FM 2 Bakeout	0%	5 d	8/11/04	8/17/04	40 d	363	365,184SS,36SS
365	Final BOOM Suite FM 2 I & T	0%	10 d	8/18/04	8/31/04	40 d	364,184	4
366	SEP Suite - CIT	31%	376 d	6/2/03	11/26/04	0 d		
367	SEP Mechanical - Sandy Shuman (GSFC)	0%	82 d	1/22/04	5/17/04	5 d		
368	SIT Electronics Enclosure (Priority 1)	0%	21 d	1/22/04	2/20/04	5 d		
369	Complete Design/Drawings for SIT Enclosure	0%	5 d	1/22/04	1/28/04	5 d		370,374
370	Fabrication of SIT Enclosure	0%	10 d	1/29/04	2/11/04	53 d	369	371
371	Inspection and Final Assembly of SIT Enclosure	0%	5 d	2/12/04	2/19/04	53 d	370	372
372	Deliver SIT Enclosure to UMD	0%	1 d	2/20/04	2/20/04	53 d	371	687,691
373	SEP FM 1 & FM 2 LVPS Boxes (Priority 2)	0%	23 d	1/29/04	3/2/04	5 d		
374	Complete Design/Drawings for SEP LVPS Boxes	0%	5 d	1/29/04	2/4/04	5 d	369	375,379
375	Fabrication of SEP LVPS Boxes	0%	10 d	2/5/04	2/19/04	62 d	374	376
376	Inspection and Final Assembly of SEP LVPS Boxes	0%	5 d	2/20/04	2/26/04	62 d	375	377
377	Deliver SEP LVPS Boxes to UCB	0%	3 d	2/27/04	3/2/04	62 d	376	126,147
378	LET Telescope (Priority 3)	0%	26 d	2/5/04	3/12/04	5 d		
379	Complete Design/Drawings for LET Telescope	0%	8 d	2/5/04	2/17/04	5 d	374	380,384
380	Fabrication of LET Telescope	0%	10 d	2/18/04	3/2/04	47 d	379	381
381	Inspection and Final Assembly of LET Telescope	0%	5 d	3/3/04	3/9/04	47 d	380	382
382	Deliver LET Telescope to CIT	0%	3 d	3/10/04	3/12/04	47 d	381	600,602
383	SEP Central Bias Supply Shields (Priority 4)	0%	20 d	2/18/04	3/16/04	5 d		
384	Complete Design/Drawings for Bias Supply Shields	0%	5 d	2/18/04	2/24/04	5 d	379	385,388
385	Fabrication of Bias Supply Shields	0%	10 d	2/25/04	3/9/04	51 d	384	386
386	Inspection and Delivery (to CIT) of Bias Supply Shields	0%	5 d	3/10/04	3/16/04	51 d	385	480,487
387	SEP Electronics Enclosure (Priority 5)	0%	31 d	2/25/04	4/7/04	5 d		
388	Complete Design/Drawings for SEP Enclosure	0%	13 d	2/25/04	3/12/04	5 d	384	389,393
389	Fabrication of SEP Enclosure	0%	10 d	3/15/04	3/26/04	47 d	388	390
390	Inspection and Final Assembly of SEP Enclosure	0%	5 d	3/29/04	4/2/04	47 d	389	391
391	Deliver SEP Enclosure to CIT	0%	3 d	4/5/04	4/7/04	47 d	390	828,841
392	HET Electronics Enclosure (Priority 6)	0%	23 d	3/15/04	4/14/04	5 d		
393	Complete Design/Drawings for HET Enclosure	0%	8 d	3/15/04	3/24/04	5 d	388	394,398
394	Fabrication of HET Enclosure	0%	10 d	3/25/04	4/7/04	23 d	393	395
395	Inspection and Final Assembly of HET Enclosure	0%	5 d	4/8/04	4/14/04	23 d	394	396
396	Deliver HET Enclosure (stays at GSFC)	0%	0 d	4/14/04	4/14/04	23 d	395	405
397	SIT Telescope (Priority 7)	0%	34 d	3/25/04	5/11/04	5 d		
398	Complete Design/Drawings for SIT Telescope	0%	18 d	3/25/04	4/19/04	5 d	393	399,403
399	Fabrication of SIT Telescope	0%	10 d	4/20/04	5/3/04	9 d	398	400
400	Inspection and Final Assembly of SIT Telescope	0%	5 d	5/4/04	5/10/04	9 d	399	401

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
401	Deliver SIT Telescope to UMD	0%	1 d	5/11/04	5/11/04	9 d	400	782,791
402	HET Telescope (Priority 8)	0%	20 d	4/20/04	5/17/04	5 d		
403	Complete Design/Drawings for HET Telescope	0%	5 d	4/20/04	4/26/04	5 d	398	404
404	Fabrication of HET Telescope	0%	10 d	4/27/04	5/10/04	5 d	403	405
405	Inspection and Final Assembly of HET Telescope	0%	5 d	5/11/04	5/17/04	5 d	404,396	406
406	Deliver HET Telescope (stays at GSFC)	0%	0 d	5/17/04	5/17/04	5 d	405	538,541
407	SEP Central - CIT	38%	176 d	8/1/03	4/14/04	68 d		
408	Deliveries	0%	31 d	3/2/04	4/14/04	68 d		
409	Deliver SEP Cen FM 1 Logic Board (stays at CIT for SEP Suite I&T)	0%	0 d	4/14/04	4/14/04	42 d	435	828
410	Deliver SEP Cen FM 2 Logic Board (stays at CIT for SEP Suite I&T)	0%	0 d	4/14/04	4/14/04	68 d	442	841
411	Deliver SEP Cen FM 1 Analog/Post-Reg Board (stays at CIT for SEP Suite	0%	0 d	4/9/04	4/9/04	45 d	460	828
412	Deliver SEP Cen FM 2 Analog/Post-Reg Board (stays at CIT for SEP Suite	0%	0 d	4/9/04	4/9/04	71 d	467	841
413	Deliver SEP Cen FM 1 SSD Bias Supply Board (stays at CIT for SEP Suite	0%	0 d	4/9/04	4/9/04	45 d	485	828
414	Deliver SEP Cen FM 2 SSD Bias Supply Board (stays at CIT for SEP Suite	0%	0 d	4/9/04	4/9/04	71 d	492	841
415	Deliver Final SEP Central Flight Software to SEP I&T	0%	0 d	3/2/04	3/2/04	73 d	495	828,841
416	Logic Board	0%	67 d	1/9/04	4/14/04	68 d		
417	Assembly	0%	26 d	1/9/04	2/17/04	86 d		
418	FM 1	0%	13 d	1/9/04	1/28/04	86 d		
419	Assemble FM 1 SEP Central Logic board	0%	5 d	1/9/04	1/15/04	86 d		420,424
420	Post-assembly QA inspection of FM 1 SEP Central Logic Board	0%	1 d	1/16/04	1/16/04	86 d	419	430
421	Rework/haywire/trim FM 1 SEP Central Logic board	0%	1 d	1/27/04	1/27/04	86 d	430	422
422	Post-rework QA inspection of FM 1 SEP Central Logic Board	0%	1 d	1/28/04	1/28/04	86 d	421	431
423	FM 2	0%	21 d	1/16/04	2/17/04	99 d		
424	Assemble FM 2 SEP Central Logic board	0%	5 d	1/16/04	1/23/04	107 d	419	425
425	Post-assembly QA inspection of FM 2 SEP Central Logic Board	0%	1 d	1/26/04	1/26/04	107 d	424	437
426	Rework/haywire/trim FM 2 SEP Central Logic board	0%	1 d	2/9/04	2/9/04	103 d	437	427
427	Post-rework QA inspection of FM 2 SEP Central Logic Boards	0%	1 d	2/17/04	2/17/04	99 d	426	438
428	Test	0%	61 d	1/20/04	4/14/04	68 d		
429	FM 1	0%	61 d	1/20/04	4/14/04	42 d		
430	Test FM 1 SEP Central Logic board (initial)	0%	5 d	1/20/04	1/26/04	86 d	420	421
431	Stake/conformal coat FM 1 SEP Central Logic board	0%	2 d	4/1/04	4/2/04	42 d	422	432
432	Bakeout FM 1 SEP Central Logic board	0%	1 d	4/5/04	4/5/04	42 d	431	433
433	Post-conf. coat QA inspection of FM 1 SEP Central Logic Board	0%	1 d	4/6/04	4/6/04	42 d	432	434
434	Test FM 1 SEP Central Logic board (final)	0%	5 d	4/7/04	4/13/04	42 d	433	435
435	FM 1 SEP Central Logic Board Mass properties/photo	0%	1 d	4/14/04	4/14/04	42 d	434	409
436	FM 2	0%	56 d	1/27/04	4/14/04	68 d		
437	Test FM 2 SEP Central Logic board (initial)	0%	5 d	1/27/04	2/2/04	107 d	425	426
438	Stake/conformal coat FM 2 SEP Central Logic board	0%	2 d	4/1/04	4/2/04	68 d	427	439
439	Bakeout FM 2 SEP Central Logic board	0%	1 d	4/5/04	4/5/04	68 d	438	440
440	Post-conf. coat QA inspection of FM 2 SEP Central Logic Board	0%	1 d	4/6/04	4/6/04	68 d	439	441

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
441	Test FM 2 SEP Central Logic board (final)	0%	5 d	4/7/04	4/13/04	68 d	440	442
442	FM 2 SEP Central Logic Board Mass properties/photo	0%	1 d	4/14/04	4/14/04	68 d	441	410
443	Analog/Post-Reg Board	24%	88 d	12/4/03	4/9/04	71 d		
444	Assembly	59%	45 d	12/4/03	2/9/04	107 d		
445	FM 1	61%	45 d	12/4/03	2/9/04	81 d		
446	Assemble Analog/Post-reg Flight Printed Circuit Board 1	85%	5 d	12/4/03	12/10/03	116 d		455,450
447	Rework/haywire/trim Analog/Post-reg Flight Board 1	0%	1 d	2/6/04	2/6/04	81 d	455	448
448	Post rework QA inspection Flight Board 1	0%	1 d	2/9/04	2/9/04	81 d	447	456
449	FM 2	57%	40 d	12/11/03	2/9/04	107 d		
450	Assemble Analog/Post-reg Flight Printed Circuit Board 2	80%	5 d	12/11/03	12/17/03	137 d	446	462
451	Rework/haywire/trim Analog/Post-reg Flight Board 2	0%	1 d	2/6/04	2/6/04	107 d	462	452
452	Post rework QA inspection Flight Board 2	0%	1 d	2/9/04	2/9/04	107 d	451	463
453	Test	0%	48 d	2/3/04	4/9/04	71 d		
454	FM 1	0%	48 d	2/3/04	4/9/04	45 d		
455	Test Analog/Post-reg Flight Board 1 (initial)	0%	3 d	2/3/04	2/5/04	81 d	446	447
456	Stake/conformal coat Analog/Post-reg Flight Board 1	0%	2 d	4/1/04	4/2/04	45 d	448	457
457	Bakeout Analog/Post-reg Flight Board 1	0%	1 d	4/5/04	4/5/04	45 d	456	458
458	Post-conf coat QA inspection Flight Board 1	0%	1 d	4/6/04	4/6/04	45 d	457	459
459	Test Analog/Post-reg Flight Board 1 (final)	0%	2 d	4/7/04	4/8/04	45 d	458	460
460	Analog/Post-reg Flight Board 1 Mass properties/photo	0%	1 d	4/9/04	4/9/04	45 d	459	411
461	FM 2	0%	48 d	2/3/04	4/9/04	71 d		
462	Test Analog/Post-reg Flight Board 2 (initial)	0%	3 d	2/3/04	2/5/04	107 d	450	451
463	Stake/conformal coat Analog/Post-reg Flight Board 2	0%	2 d	4/1/04	4/2/04	71 d	452	464
464	Bakeout Analog/Post-reg Flight Board 2	0%	1 d	4/5/04	4/5/04	71 d	463	465
465	Post-conf coat QA inspection Flight Board 2	0%	1 d	4/6/04	4/6/04	71 d	464	466
466	Test Analog/Post-reg Flight Board 2 (final)	0%	2 d	4/7/04	4/8/04	71 d	465	467
467	Analog/Post-reg Flight Board 2 Mass properties/photo	0%	1 d	4/9/04	4/9/04	71 d	466	412
468	SEP Solid State Detector Bias Supply Board	0%	63 d	1/12/04	4/9/04	71 d		
469	Assembly	0%	53 d	1/12/04	3/26/04	74 d		
470	FM 1	0%	50 d	1/12/04	3/23/04	51 d		
471	Assemble flight Solid State Detector Bias Supply Printed Circuit B	0%	10 d	1/12/04	1/26/04	86 d		475,480
472	Rework/haywire/trim Solid State Detector Bias Supply flight board	0%	1 d	3/22/04	3/22/04	51 d	480	473
473	Post rework QA inspection of Solid State Detector Bias Supply bo	0%	1 d	3/23/04	3/23/04	51 d	472	481
474	FM 2	0%	43 d	1/27/04	3/26/04	74 d		
475	Assemble flight Solid State Detector Bias Supply Printed Circuit B	0%	10 d	1/27/04	2/9/04	102 d	471	487
476	Rework/haywire/trim Solid State Detector Bias Supply flight board	0%	1 d	3/25/04	3/25/04	74 d	487	477
477	Post rework QA inspection of Solid State Detector Bias Supply bo	0%	1 d	3/26/04	3/26/04	74 d	476	488
478	Test	0%	18 d	3/17/04	4/9/04	51 d		
479	FM 1	0%	18 d	3/17/04	4/9/04	45 d		
480	Test Solid State Detector Bias Supply flight board 1 (initial)	0%	3 d	3/17/04	3/19/04	51 d	471,386	472,487

ID		Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
481		Stake/conformal coat Solid State Detector Bias Supply flight boar	0%	2 d	4/1/04	4/2/04	45 d	473	482
482		Bakeout Solid State Detector Bias Supply flight board 1	0%	1 d	4/5/04	4/5/04	45 d	481	483
483		Post-conf coat QA inspection of Solid State Detector Bias Supply	0%	1 d	4/6/04	4/6/04	45 d	482	484
484		Test Solid State Detector Bias Supply flight board 1 (final)	0%	2 d	4/7/04	4/8/04	45 d	483	485
485		Solid State Detector Bias Supply FM 1 Mass properties/photo	0%	1 d	4/9/04	4/9/04	45 d	484	413
486		FM 2	0%	15 d	3/22/04	4/9/04	71 d		
487		Test Solid State Detector Bias Supply flight board 2 (initial)	0%	3 d	3/22/04	3/24/04	74 d	475,386,480	476
488		Stake/conformal coat Solid State Detector Bias Supply flight boar	0%	2 d	4/1/04	4/2/04	71 d	477	489
489		Bakeout Solid State Detector Bias Supply flight board 2	0%	1 d	4/5/04	4/5/04	71 d	488	490
490		Post-conf coat QA inspection of Solid State Detector Bias Supply	0%	1 d	4/6/04	4/6/04	71 d	489	491
491		Test Solid State Detector Bias Supply flight board 2 (final)	0%	2 d	4/7/04	4/8/04	71 d	490	492
492		Solid State Detector Bias Supply FM 2 Mass properties/photo	0%	1 d	4/9/04	4/9/04	71 d	491	414
493		Flight Software	65%	145 d	8/1/03	3/2/04	73 d		
494		SEP Central Build 3	90%	105 d	8/1/03	1/2/04	73 d		495
495		Final Flight S/W Build	0%	40 d	1/5/04	3/2/04	73 d	494	415
496		HET - GSFC	0%	199 d	10/1/03	7/15/04	82 d		
497		Deliveries	0%	199 d	10/1/03	7/15/04	92 d		
498		Deliver HET FM 1 (to CIT for SEP Suite I&T)	0%	2 d	6/16/04	6/17/04	19 d	539	832
499		Deliver HET FM 2 (to CIT for SEP Suite I&T)	0%	2 d	7/14/04	7/15/04	5 d	542	845
500		Deliver Final HET Flight Software to SEP I&T	0%	2 d	10/1/03	10/2/03	289 d		
501		Detectors	0%	60 d	1/6/04	3/31/04	38 d		
502		Receive & Inspect HET Flight Detectors	0%	40 d	1/6/04	3/3/04	38 d		504SS
503		Detector Testing	0%	60 d	1/6/04	3/31/04	38 d		
504		Capacitance, Current, Depletion, Alpha Resin Testing	0%	60 d	1/6/04	3/31/04	38 d	502SS	505SS
505		Life Test HET Detectors	0%	60 d	1/6/04	3/31/04	38 d	504SS	538,541
506		Electronics Board	0%	82 d	1/22/04	5/17/04	7 d		
507		Assembly	0%	44 d	1/22/04	3/24/04	7 d		
508		Install Socket on HET Electronics FM 1 Board for ETU ACTEL	0%	1 d	1/22/04	1/22/04	7 d		509
509		Complete Population of HET Electronics FM 1 Board	0%	1 d	1/23/04	1/23/04	7 d	508	513,510
510		Populate HET Electronics FM 2 Board	0%	10 d	3/11/04	3/24/04	7 d	509,516,561	522
511		Test	0%	80 d	1/26/04	5/17/04	7 d		
512		FM 1	0%	78 d	1/26/04	5/13/04	7 d		
513		Test HET FM 1 Electronics Board w/ETU ACTEL	0%	20 d	1/26/04	2/23/04	7 d	509	522,514
514		Remove ETU ACTEL and socket from HET FM 1 Electronics Boa	0%	1 d	2/24/04	2/24/04	7 d	513	515
515		Install Flight ACTEL on HET FM 1 Electronics Board	0%	1 d	2/25/04	2/25/04	7 d	514	516
516		Retest HET FM 1 Electronics Board w/Flight ACTEL	0%	10 d	2/26/04	3/10/04	7 d	515,561	517,510
517		Conformal Coat HET FM 1 Electronics Board	0%	2 d	3/11/04	3/12/04	45 d	516	518
518		Retest Conformal Coated HET FM 1 Electronics Board	0%	2 d	5/6/04	5/7/04	7 d	517,522	519,524
519		Bakeout HET FM 1 Electronics Board	0%	2 d	5/10/04	5/11/04	7 d	518	520
520		Retest Bakedout HET FM 1 Electronics Board	0%	2 d	5/12/04	5/13/04	7 d	519,524	526,538

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
521	FM 2	0%	38 d	3/25/04	5/17/04	7 d		
522	Test HET FM 2 Electronics Board	0%	30 d	3/25/04	5/5/04	7 d	510,513	523,518
523	Conformal Coat HET FM 2 Electronics Board	0%	2 d	5/6/04	5/7/04	7 d	522	524
524	Retest Conformal Coated HET FM 2 Electronics Board	0%	2 d	5/10/04	5/11/04	7 d	523,518	525,520
525	Bakeout HET FM 2 Electronics Board	0%	2 d	5/12/04	5/13/04	20 d	524	526
526	Retest Bakedout HET FM 2 Electronics Board	0%	2 d	5/14/04	5/17/04	20 d	525,520	541
527	Flight Software	0%	70 d	1/22/04	4/29/04	17 d		
528	Design	0%	10 d	1/22/04	2/4/04	17 d		
529	Complete Definition of HET PH Event Processing	0%	10 d	1/22/04	2/4/04	17 d		531
530	Coding	0%	20 d	2/5/04	3/4/04	17 d		
531	Code HET PH Event Processing Update for Flight	0%	10 d	2/5/04	2/19/04	17 d	529	532
532	Code HET Telemetry Formatting Update for Flight	0%	10 d	2/20/04	3/4/04	17 d	531	534
533	Test	0%	40 d	3/5/04	4/29/04	17 d		
534	Test Integrated HET S/W with Simulated Data	0%	15 d	3/5/04	3/25/04	17 d	532	535
535	Verify HET On-Board Software	0%	25 d	3/26/04	4/29/04	17 d	534	538,541
536	Integration & Test	0%	40 d	5/18/04	7/13/04	5 d		
537	FM 1	0%	20 d	5/18/04	6/15/04	5 d		
538	Integrate HET FM 1 Electronics Telescope (Ahead)	0%	5 d	5/18/04	5/24/04	5 d	520,406,505,535	539,541
539	Test HET FM 1 Telescope (Ahead)	0%	15 d	5/25/04	6/15/04	5 d	538	542,498
540	FM 2	0%	35 d	5/25/04	7/13/04	5 d		
541	Integrate HET FM 2 Electronics Telescope (Behind)	0%	5 d	5/25/04	6/1/04	15 d	526,538,406,505,535	542
542	Test HET FM 2 Telescope (Behind)	0%	20 d	6/16/04	7/13/04	5 d	539,541	499
543	LET - CIT	51%	207 d	7/14/03	5/7/04	53 d		
544	Deliveries	0%	79 d	1/15/04	5/7/04	53 d		
545	Deliver LET FM 1 (stays at CIT for SEP I&T)	0%	0 d	5/7/04	5/7/04	47 d	588,600	831
546	Deliver LET FM 2 (stays at CIT for SEP I&T)	0%	0 d	5/7/04	5/7/04	53 d	595,602	844
547	Deliver LET Flight Software Build 3	0%	0 d	1/15/04	1/15/04	126 d	597	831,844
548	Detectors	48%	142 d	7/14/03	2/5/04	72 d		
549	Detector Testing	42%	136 d	7/14/03	1/28/04	78 d		
550	Perform Detector Noise & breakdown Testing	35%	76 d	9/2/03	12/19/03	103 d		551SS+9 d,552SS,600
551	Perform Detector Thermal Vac Testing	40%	75 d	9/15/03	1/2/04	95 d	550SS+9 d	553SS,600,602
552	Perform Detector Alphas Testing	35%	76 d	9/2/03	12/19/03	103 d	550SS	553SS,600,602
553	Analyze Detector Testing Data	35%	75 d	9/15/03	1/2/04	95 d	551SS,552SS	600,602
554	Capacitance, Current, Depletion, Alpha Resolution	50%	136 d	7/14/03	1/28/04	78 d		555SS+10 d,600,602
555	Life Test LET Flight Detectors	50%	76 d	7/28/03	11/13/03	128 d	554SS+10 d	600,602
556	Hybrids	72%	128 d	8/1/03	2/5/04	20 d		
557	Package and Test Flight Hybrids Lot 1 (7-10 Parts)	100%	21 d	8/1/03	8/29/03	0 d		558,560
558	Package and Test Flight Hybrids Lot 2 (10 Parts)	100%	24 d	9/2/03	10/3/03	0 d	557	559SS+10 d,560
559	Package and Test Flight Hybrids Lot 3 (20 Parts)	60%	83 d	9/16/03	1/15/04	30 d	558SS+10 d	560
560	Select parts for flight	0%	2 d	2/2/04	2/3/04	20 d	559,557,558	561

ID		Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
561		Deliver HET Hybrids to GSFC	0%	2 d	2/4/04	2/5/04	20 d	560	510,516
562		Detector/Misc Board	62%	146 d	9/15/03	4/14/04	70 d		
563		Remaining Procurements	99%	95 d	9/15/03	2/2/04	80 d		
564		Procure LET Detector Board Flight Parts	99%	95 d	9/15/03	2/2/04	80 d		567
565		Procure flight LET Detector Printed Circuit Board	100%	2 d	10/14/03	10/15/03	0 d		568FS+18 d
566		Fabrication	40%	58 d	11/12/03	2/5/04	80 d		
567		Receive/inspect/database flight parts for LET Detector Board	0%	2 d	2/3/04	2/4/04	80 d	564	569,572
568		Inspect flight LET Detector Printed Circuit Boards (coupons & elec con	100%	2 d	11/12/03	11/13/03	0 d	565FS+18 d	572
569		Burn-in flight FPGA/EEPROM for LET Detector Board	0%	1 d	2/5/04	2/5/04	80 d	567	572
570		Assembly	0%	48 d	2/6/04	4/14/04	70 d		
571		FM 1	0%	18 d	2/6/04	3/3/04	80 d		
572		Assemble LET FM 1 Detector Board	0%	10 d	2/6/04	2/20/04	80 d	567,568,569	573,577
573		Post-assembly QA inspection for flight LET Detector Boards	0%	1 d	2/23/04	2/23/04	84 d	572	583
574		Rework/haywire/trim flight LET Detector Boards	0%	1 d	3/2/04	3/2/04	84 d	583	575
575		Post-rework QA inspection for LET Detector Boards	0%	1 d	3/3/04	3/3/04	84 d	574	584
576		FM 2	0%	18 d	2/23/04	3/17/04	80 d		
577		Assemble LET FM 2 Detector Board	0%	10 d	2/23/04	3/5/04	80 d	572	578
578		Post-assembly QA inspection for flight LET Detector Boards	0%	1 d	3/8/04	3/8/04	80 d	577	590
579		Rework/haywire/trim flight LET Detector Boards	0%	1 d	3/16/04	3/16/04	80 d	590	580
580		Post-rework QA inspection for LET Detector Boards	0%	1 d	3/17/04	3/17/04	80 d	579	591
581		Test	0%	37 d	2/24/04	4/14/04	70 d		
582		FM 1	0%	37 d	2/24/04	4/14/04	64 d		
583		Test LET FM 1 Detector Boards (initial)	0%	5 d	2/24/04	3/1/04	84 d	573	574
584		Stake/conformal coat LET FM 1 Detector Boards	0%	2 d	4/1/04	4/2/04	64 d	575	585
585		Bakeout LET FM 1 Detector Boards	0%	1 d	4/5/04	4/5/04	64 d	584	586
586		Post-conf. Coat QA inspection of LET FM 1 Detector Boards	0%	1 d	4/6/04	4/6/04	64 d	585	587
587		Test LET FM 1 Detector Boards (final)	0%	5 d	4/7/04	4/13/04	64 d	586	588
588		LET FM 1 Detector Board Mass properties/photo	0%	1 d	4/14/04	4/14/04	64 d	587	545
589		FM 2	0%	27 d	3/9/04	4/14/04	70 d		
590		Test LET FM 2 Detector Boards (initial)	0%	5 d	3/9/04	3/15/04	80 d	578	579
591		Stake/conformal coat LET FM 2 Detector Boards	0%	2 d	4/1/04	4/2/04	70 d	580	592
592		Bakeout LET FM 2 Detector Boards	0%	1 d	4/5/04	4/5/04	70 d	591	593
593		Post-conf. Coat QA inspection of LET FM 2 Detector Boards	0%	1 d	4/6/04	4/6/04	70 d	592	594
594		Test LET FM 2 Detector Boards (final)	0%	5 d	4/7/04	4/13/04	70 d	593	595
595		LET FM 2 Detector Board Mass properties/photo	0%	1 d	4/14/04	4/14/04	70 d	594	546
596		Flight Software	90%	114 d	8/1/03	1/15/04	126 d		
597		Let Flight Software Build 3	90%	114 d	8/1/03	1/15/04	126 d		547
598		Integration & Test	0%	40 d	3/15/04	5/7/04	47 d		
599		FM 1	0%	40 d	3/15/04	5/7/04	47 d		
600		Integrate LET FM 1 Housing With Detectors	0%	40 d	3/15/04	5/7/04	47 d	550,551,552,553,5	545

ID		Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
601		FM 2	0%	40 d	3/15/04	5/7/04	53 d		
602		Integrate LET FM 2 Housing With Detectors	0%	40 d	3/15/04	5/7/04	53 d	550,551,552,553,€	546
603		SEPT - Kiel/ESTEC	35%	260 d	8/8/03	8/18/04	68 d		
604		Deliveries	0%	122 d	3/1/04	8/18/04	49 d		
605		Deliver SEPT-NS FM 1 to CIT for SEP I&T and Suite EMC	0%	5 d	3/12/04	3/18/04	81 d	649,650	829
606		Deliver SEPT-E FM 1 to CIT for SEP I&T and Suite EMC	0%	5 d	3/1/04	3/5/04	90 d	656,657	829
607		Final Delivery of SEPT FM 1 to APL	0%	2 d	8/17/04	8/18/04	35 d	651,658	5
608		Deliver SEPT-NS FM 2 to CIT for SEP I&T and Suite EMC	0%	5 d	3/8/04	3/12/04	91 d	664,665	842
609		Deliver SEPT-E FM 2 to CIT for SEP I&T and Suite EMC	0%	5 d	3/8/04	3/12/04	91 d	671,672	842
610		Final Delivery of SEPT FM 2 to APL	0%	2 d	8/17/04	8/18/04	49 d	666,673	6
611		Mechanical Assembly	66%	46 d	12/22/03	2/27/04	190 d		
612	✓	FM 1	100%	13 d	12/22/03	1/9/04	0 d		
613	✓	Assemble SEPT-NS FM 1 Sensor and E-Box Housing	100%	13 d	12/22/03	1/9/04	0 d		616SS+4 d,646
614	✓	Assemble SEPT-E FM 1 Sensor and E-Box Housing	100%	13 d	12/22/03	1/9/04	0 d		617SS+4 d,653
615	✓	FM 2	100%	14 d	12/29/03	1/16/04	0 d		
616	✓	Assemble SEPT-NS FM 2 Sensor and E-Box Housing	100%	14 d	12/29/03	1/16/04	0 d	613SS+4 d	619,661
617	✓	Assemble SEPT-E FM 2 Sensor and E-Box Housing	100%	14 d	12/29/03	1/16/04	0 d	614SS+4 d	619,668
618		Spare	0%	28 d	1/20/04	2/27/04	190 d		
619		Assemble SEPT Spare Sensor and E-Box Housing	0%	28 d	1/20/04	2/27/04	190 d	616,617	
620		Electronics	43%	144 d	8/8/03	3/8/04	184 d		
621		Assembly	44%	124 d	8/8/03	2/6/04	184 d		
622	✓	FM 2	100%	106 d	8/8/03	1/12/04	0 d		
623	✓	Assemble SEPT-NS FM 2 Electronics	100%	106 d	8/8/03	1/12/04	0 d		626SS,630
624	✓	Assemble SEPT-E FM 2 Electronics	100%	92 d	8/8/03	12/19/03	0 d		627SS,631FS-7 d
625		Spare	0%	124 d	8/8/03	2/6/04	184 d		
626		Assemble SEPT-NS FS Electronics	0%	124 d	8/8/03	2/6/04	184 d	623SS	637
627		Assemble SEPT-E FS Electronics	0%	124 d	8/8/03	2/6/04	184 d	624SS	638
628		Test	35%	59 d	12/11/03	3/8/04	184 d		
629		FM 2	74%	34 d	12/11/03	1/30/04	209 d		
630	✓	SEPT-NS FM 2 Electronics Test	100%	4 d	1/13/04	1/16/04	0 d	623	632
631	✓	SEPT-E FM 2 Electronics Test	100%	4 d	12/11/03	12/16/03	0 d	624FS-7 d	633
632		Coat SEPT-NS FM 2 Electronics Board	0%	4 d	1/20/04	1/23/04	209 d	630	634
633	✓	Coat SEPT-E FM 2 Electronics Board	100%	5 d	12/17/03	12/23/03	0 d	631	635
634		SEPT-NS FM 2 Mechanical Integration	0%	5 d	1/26/04	1/30/04	209 d	632	
635	✓	SEPT-E FM 2 Mechanical Integration	100%	13 d	12/24/03	1/13/04	0 d	633	
636		Spare	0%	20 d	2/9/04	3/8/04	184 d		
637		SEPT-NS Flight Spare Electronics Test	0%	5 d	2/9/04	2/13/04	184 d	626	639
638		SEPT-E Flight Spare Electronics Test	0%	5 d	2/9/04	2/13/04	184 d	627	640
639		Coat SEPT-NS FS Electronics Board	0%	5 d	2/17/04	2/23/04	184 d	637	641
640		Coat SEPT-E FS Electronics Board	0%	5 d	2/17/04	2/23/04	184 d	638	642

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
641	SEPT-NS FS Mechanical Integration	0%	10 d	2/24/04	3/8/04	184 d	639	
642	SEPT-E FS Mechanical Integration	0%	10 d	2/24/04	3/8/04	184 d	640	
643	Integration & Test	3%	153 d	1/12/04	8/16/04	49 d		
644	FM 1	6%	153 d	1/12/04	8/16/04	35 d		
645	SEPT-NS	0%	144 d	1/26/04	8/16/04	35 d		
646	SEPT-NS FM 1 Integration & Test	0%	5 d	1/26/04	1/30/04	81 d	613	647
647	Muon and Radioactive Source Test	0%	5 d	2/2/04	2/6/04	81 d	646	648
648	SEPT-NS FM 1 Vibration and Acoustics	0%	10 d	2/9/04	2/23/04	81 d	647	649,38SS
649	SEPT-NS FM 1 Thermal Vac	0%	10 d	2/24/04	3/8/04	81 d	648	605,650,39SS
650	SEPT-NS FM 1 Bake Out	0%	3 d	3/9/04	3/11/04	81 d	649	605,40SS
651	SEPT-NS FM 1 Thermal Balance	0%	10 d	8/3/04	8/16/04	35 d	16	607,41SS
652	SEPT-E	12%	153 d	1/12/04	8/16/04	35 d		
653	SEPT-E FM 1 Integration & Test	100%	5 d	1/12/04	1/16/04	0 d	614	654
654	Muon and Radioactive Source Test	0%	5 d	1/20/04	1/26/04	90 d	653	655
655	SEPT-E FM 1 Vibration and Acoustics	0%	10 d	1/27/04	2/9/04	90 d	654	656,38SS
656	SEPT-E FM 1 Thermal Vac	0%	10 d	2/10/04	2/24/04	90 d	655	606,657,39SS
657	SEPT-E FM 1 Bake Out	0%	3 d	2/25/04	2/27/04	90 d	656	606,40SS
658	SEPT-E FM 1 Thermal Balance	0%	10 d	8/3/04	8/16/04	35 d	16	607,41SS
659	FM 2	0%	148 d	1/20/04	8/16/04	49 d		
660	SEPT-NS	0%	148 d	1/20/04	8/16/04	49 d		
661	SEPT-NS FM 2 Integration & Test	0%	5 d	1/20/04	1/26/04	91 d	616	662
662	Muon and Radioactive Source Test	0%	5 d	1/27/04	2/2/04	91 d	661	663
663	SEPT-NS FM 2 Vibration and Acoustics	0%	10 d	2/3/04	2/17/04	91 d	662	664,52SS
664	SEPT-NS FM 2 Thermal Vac	0%	10 d	2/18/04	3/2/04	91 d	663	608,665,53SS
665	SEPT-NS FM 2 Bake Out	0%	3 d	3/3/04	3/5/04	91 d	664	608,54SS
666	SEPT-NS FM 2 Thermal Balance	0%	10 d	8/3/04	8/16/04	49 d	16	610,56SS
667	SEPT-E	0%	148 d	1/20/04	8/16/04	49 d		
668	SEPT-E FM 2 Integration & Test	0%	5 d	1/20/04	1/26/04	91 d	617	669
669	Muon and Radioactive Source Test	0%	5 d	1/27/04	2/2/04	91 d	668	670
670	SEPT-E FM 2 Vibration and Acoustics	0%	10 d	2/3/04	2/17/04	91 d	669	671,52SS
671	SEPT-E FM 2 Thermal Vac	0%	10 d	2/18/04	3/2/04	91 d	670	609,672,53SS
672	SEPT-E FM 2 Bake Out	0%	3 d	3/3/04	3/5/04	91 d	671	609,54SS
673	SEPT-E FM 2 Thermal Balance	0%	10 d	8/3/04	8/16/04	49 d	16	610,56SS
674	SIT - UMD	27%	348 d	6/2/03	10/15/04	28 d		
675	Deliveries	0%	57 d	7/13/04	9/30/04	10 d		
676	Deliver SIT FM 1 to CIT for SEP I&T and Suite EMC	0%	2 d	7/13/04	7/14/04	10 d	774	812,837
677	Deliver SIT FM 1 back to GSFC for Environmental Testing	0%	2 d	8/3/04	8/4/04	10 d	16	799
678	Final Delivery of SIT FM 1 to APL	0%	1 d	9/30/04	9/30/04	5 d	806	5
679	Deliver SIT FM 2 to CIT for SEP I&T	0%	2 d	7/20/04	7/21/04	9 d	779	812,850
680	Deliver SIT FM 2 back to GSFC for Environmental Testing	0%	2 d	8/10/04	8/11/04	5 d	865	799




ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
681	Final Delivery of SIT FM 2 to APL	0%	1 d	9/30/04	9/30/04	19 d	806	6
682	Detectors	0%	43 d	1/5/04	3/5/04	58 d		
683	Detector Testing	0%	43 d	1/5/04	3/5/04	58 d		
684	FM 1	0%	43 d	1/5/04	3/5/04	53 d		
685	Test FM 1 Solid States Detectors	0%	24 d	1/5/04	2/6/04	62 d		686SS,689SS,687
686	Test FM 1 Foils	0%	24 d	1/5/04	2/6/04	62 d	685SS	690SS,687
687	Alpha Tests FM 1 w/Proto Elect	0%	10 d	2/23/04	3/5/04	53 d	685,686,372	781
688	FM 2	0%	43 d	1/5/04	3/5/04	58 d		
689	Test FM 2 Solid States Detectors	0%	24 d	1/5/04	2/6/04	67 d	685SS	691
690	Test FM 2 Foils	0%	24 d	1/5/04	2/6/04	67 d	686SS	691
691	Alpha Tests FM 2 w/Proto Elect	0%	10 d	2/23/04	3/5/04	58 d	689,690,372	790
692	ACTEL - for SIT Logic Board	0%	11 d	2/27/04	3/12/04	81 d		
693	Burn Final SIT ACTEL Chip (at GSFC)	0%	1 d	2/27/04	2/27/04	81 d		694
694	Evaluate (Test) SIT Final ACTEL Chip	0%	10 d	3/1/04	3/12/04	81 d	693	724,729
695	TOF Boards	0%	20 d	1/30/04	2/27/04	66 d		
696	Receive Analog and Digital TOF Boards (FM 1 & FM 2)	0%	0 d	1/30/04	1/30/04	66 d		699,701
697	Test	0%	20 d	1/30/04	2/27/04	66 d		
698	FM 1	0%	10 d	1/30/04	2/12/04	66 d		
699	FM 1 TOF Boards Functional Test	0%	10 d	1/30/04	2/12/04	66 d	696	701,782
700	FM 2	0%	10 d	2/13/04	2/27/04	66 d		
701	FM 2 TOF Boards Functional Test	0%	10 d	2/13/04	2/27/04	66 d	696,699	791
702	Energy Board	57%	76 d	11/10/03	3/2/04	64 d		
703	Assembly	76%	46 d	11/10/03	1/16/04	79 d		
704	FM 1	76%	46 d	11/10/03	1/16/04	64 d		
705	Assemble FM 1 Energy Board	76%	46 d	11/10/03	1/16/04	64 d		710,707SS
706	FM 2	76%	46 d	11/10/03	1/16/04	79 d		
707	Assemble FM 2 Energy Board	76%	46 d	11/10/03	1/16/04	79 d	705SS	714
708	Test	0%	30 d	1/20/04	3/2/04	64 d		
709	FM 1	0%	15 d	1/20/04	2/9/04	64 d		
710	Bench Test/Calibration	0%	10 d	1/20/04	2/2/04	64 d	705	711
711	Thermal	0%	3 d	2/3/04	2/5/04	64 d	710	712
712	Voltage Margin	0%	2 d	2/6/04	2/9/04	64 d	711	714,782
713	FM 2	0%	15 d	2/10/04	3/2/04	64 d		
714	FM 2 Energy Board Functional Test	0%	15 d	2/10/04	3/2/04	64 d	707,712	791
715	Logic Board	31%	165 d	11/10/03	7/6/04	9 d		
716	Design	100%	3 d	11/17/03	11/19/03	0 d		
717	Layout new Logic Board Design	100%	3 d	11/17/03	11/19/03	0 d		719
718	Fabrication	0%	10 d	2/2/04	2/13/04	24 d		
719	FM 1 & FM 2 Logic Board Fabrication	0%	10 d	2/2/04	2/13/04	24 d	717,733	735
720	Assembly	0%	88 d	3/2/04	7/2/04	9 d		

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
721	FM 1	0%	83 d	3/2/04	6/25/04	10 d		
722	Assemble FM 1 Logic Board with ETU ACTELS	0%	11 d	3/2/04	3/16/04	24 d	735	737
723	Disassemble FM 1 Logic Board with ETU ACTELS	0%	1 d	6/22/04	6/22/04	10 d	788	728SS,724
724	Retrofit/Inspect FM 1 Flight ACTELS	0%	2 d	6/23/04	6/24/04	10 d	723,694	725
725	Reassemble FM 1 Logic Board with Flight ACTELS	0%	1 d	6/25/04	6/25/04	10 d	724	738
726	FM 2	0%	88 d	3/2/04	7/2/04	9 d		
727	Assemble FM 2 Logic Board with ETU ACTELS	0%	11 d	3/2/04	3/16/04	39 d	735	740
728	Disassemble FM 2 Logic Boards	0%	1 d	6/29/04	6/29/04	9 d	797,723SS	729
729	Retrofit/Inspect Flight ACTELS	0%	2 d	6/30/04	7/1/04	9 d	728,694	730
730	Reassemble FM 2 Logic Boards	0%	1 d	7/2/04	7/2/04	9 d	729	741
731	Test	42%	165 d	11/10/03	7/6/04	9 d		
732	ETU	75%	55 d	11/10/03	1/30/04	24 d		
733	Test New ETU Logic Board	75%	55 d	11/10/03	1/30/04	24 d		719
734	FM1 & FM 2	0%	10 d	2/17/04	3/1/04	24 d		
735	Coupon Testing	0%	10 d	2/17/04	3/1/04	24 d	719	722,727
736	FM 1	0%	74 d	3/17/04	6/29/04	10 d		
737	Functional Test of FM 1 Logic Board w/ETU ACTELS	0%	15 d	3/17/04	4/6/04	24 d	722	740,782
738	Functional Test of FM 1 Logic Board w/Flight ACTELS	0%	2 d	6/28/04	6/29/04	10 d	725	771
739	FM 2	0%	64 d	4/7/04	7/6/04	9 d		
740	Functional Test of FM 2 Logic Board w/ETU ACTELS	0%	15 d	4/7/04	4/27/04	24 d	727,737	791
741	Functional Test of FM 2 Logic Board w/Flight ACTELS	0%	2 d	7/5/04	7/6/04	9 d	730	776
742	Mother Board	0%	50 d	12/22/03	3/4/04	49 d		
743	Design	0%	15 d	12/22/03	1/13/04	49 d		
744	FM 1 & FM 2	0%	15 d	12/22/03	1/13/04	49 d		
745	Finalize Mother Board Layout	0%	15 d	12/22/03	1/13/04	49 d		748
746	Fabrication	0%	15 d	1/14/04	2/4/04	49 d		
747	FM 1 & FM 2	0%	15 d	1/14/04	2/4/04	49 d		
748	Fabricate FM 1 & FM 2 Mother Boards	0%	15 d	1/14/04	2/4/04	49 d	745	750,754
749	Assembly	0%	10 d	2/5/04	2/19/04	49 d		
750	Assemble FM 1 Mother Board	0%	10 d	2/5/04	2/19/04	49 d	748	751SS,756
751	Assemble FM 2 Mother Board	0%	10 d	2/5/04	2/19/04	54 d	750SS	758
752	Test	0%	20 d	2/5/04	3/4/04	59 d		
753	FM 1 & FM 2	0%	15 d	2/5/04	2/26/04	191 d		
754	Coupon Testing of Mother Boards	0%	15 d	2/5/04	2/26/04	191 d	748	
755	FM 1	0%	5 d	2/20/04	2/26/04	49 d		
756	Functional Test FM 1 Mother Board	0%	5 d	2/20/04	2/26/04	49 d	750	758,782
757	FM 2	0%	5 d	2/27/04	3/4/04	49 d		
758	Functional Test FM 2 Mother Board	0%	5 d	2/27/04	3/4/04	49 d	751,756	764,791
759	HVPS	0%	62 d	1/12/04	4/8/04	34 d		
760	Test	0%	62 d	1/12/04	4/8/04	34 d		

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
761	FM 1	0%	10 d	1/12/04	1/26/04	81 d		
762	Functional Test SIT FM 1 HVPS	0%	10 d	1/12/04	1/26/04	81 d	72	781
763	FM 2	0%	10 d	3/26/04	4/8/04	34 d		
764	Functional Test SIT FM 2 HVPS	0%	10 d	3/26/04	4/8/04	34 d	78,758	790
765	Flight Software	70%	210 d	6/2/03	4/1/04	65 d		
766	Test	70%	210 d	6/2/03	4/1/04	65 d		
767	FM 1 & FM 2	70%	210 d	6/2/03	4/1/04	65 d		
768	Validate SIT On-Board Code	70%	210 d	6/2/03	4/1/04	65 d		788
769	Integration & Test	0%	95 d	3/8/04	7/19/04	9 d		
770	FM 1	0%	9 d	6/30/04	7/12/04	10 d		
771	Disassemble SIT FM 1	0%	1 d	6/30/04	6/30/04	10 d	738	772
772	Conformal Coat SIT FM 1 Boards	0%	2 d	7/1/04	7/2/04	10 d	771	773,137SS
773	Reassemble SIT FM 1	0%	1 d	7/5/04	7/5/04	10 d	772	774
774	Post-Coat Retest SIT FM 1	0%	5 d	7/6/04	7/12/04	10 d	773	676
775	FM 2	0%	9 d	7/7/04	7/19/04	9 d		
776	Disassemble SIT FM 2	0%	1 d	7/7/04	7/7/04	9 d	741	777
777	Conformal Coat SIT FM 2 Boards	0%	2 d	7/8/04	7/9/04	9 d	776	778
778	Reassemble SIT FM 2	0%	1 d	7/12/04	7/12/04	9 d	777	779
779	Post-Coat Retest SIT FM 2	0%	5 d	7/13/04	7/19/04	9 d	778	679
780	FM 1	0%	75 d	3/8/04	6/21/04	10 d		
781	Integrate SIT FM 1 HVPS and SIT FM 1 Telescope Without Electronic	0%	3 d	3/8/04	3/10/04	53 d	762,687	782
782	Integrate SIT FM 1 Telescope	0%	2 d	5/12/04	5/13/04	9 d	737,699,756,712,7	783
783	SIT FM 1 Debug	0%	5 d	5/14/04	5/20/04	9 d	782	784
784	SIT FM 1 Bench Test	0%	5 d	5/21/04	5/27/04	9 d	783	785
785	SIT FM 1 Alpha TEST	0%	5 d	5/28/04	6/4/04	9 d	784	786
786	SIT FM 1 Noise Removal	0%	5 d	6/7/04	6/11/04	9 d	785	787,795
787	SIT FM 1 Thermal Test	0%	5 d	6/14/04	6/18/04	10 d	786	788
788	SIT FM 1 Functional Test	0%	1 d	6/21/04	6/21/04	10 d	787,768	723
789	FM 2	0%	56 d	4/9/04	6/28/04	9 d		
790	Integrate SIT FM 2 HVPS and SIT FM 2 Telescope Without Electronic	0%	3 d	4/9/04	4/13/04	34 d	764,691	791
791	Integrate SIT FM 2 Telescope	0%	2 d	5/12/04	5/13/04	14 d	740,701,758,714,7	792
792	SIT FM 2 Debug	0%	5 d	5/14/04	5/20/04	14 d	791	793
793	SIT FM 2 Bench Test	0%	5 d	5/21/04	5/27/04	14 d	792	794
794	SIT FM 2 Alpha Test	0%	5 d	5/28/04	6/4/04	14 d	793	795
795	SIT FM 2 Noise Removal	0%	5 d	6/14/04	6/18/04	9 d	794,786	796
796	SIT FM 2 Thermal Test	0%	5 d	6/21/04	6/25/04	9 d	795	797
797	SIT FM 2 Functional Test	0%	1 d	6/28/04	6/28/04	9 d	796	728
798	Environmental Testing at GSFC (FM 1 & FM 2)	0%	34 d	8/12/04	9/29/04	5 d		
799	SIT FM 1 & FM 2 Vibration and Acoustics	0%	10 d	8/12/04	8/25/04	5 d	677,680	800,47SS,61SS
800	Functional Test	0%	1 d	8/26/04	8/26/04	5 d	799	801

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
801	SIT FM 1 & FM 2 Thermal Balance	0%	5 d	8/27/04	9/2/04	5 d	800	802,48SS,62SS
802	SIT FM 1 & FM 2 Thermal Vac	0%	10 d	9/3/04	9/17/04	5 d	801	803,49SS,63SS
803	Disassemble SIT Instruments	0%	2 d	9/20/04	9/21/04	5 d	802	804
804	Bake Out	0%	3 d	9/22/04	9/24/04	5 d	803	805,50SS,64SS
805	Reassemble Instruments	0%	2 d	9/27/04	9/28/04	5 d	804	806
806	Functional Test	0%	1 d	9/29/04	9/29/04	5 d	805	678,681
807	Emulators/Simulators/GSE	9%	211 d	12/17/03	10/15/04	28 d		
808	Simulators	13%	80 d	12/17/03	4/12/04	159 d		
809	Labview CPU Ready	13%	80 d	12/17/03	4/12/04	159 d		810SS
810	Test Simulator	13%	80 d	12/17/03	4/12/04	159 d	809SS	
811	GSE	0%	60 d	7/22/04	10/15/04	28 d		
812	Pulse Generator	0%	60 d	7/22/04	10/15/04	28 d	676,679	
813	SEP Suite I&T - CIT	28%	333 d	8/1/03	11/26/04	0 d		
814	ETU	0%	5 d	2/23/04	2/27/04	190 d		
815	Complete Integration/Test of SEP Central EM with Sensors	0%	5 d	2/23/04	2/27/04	190 d		
816	FM 1 & FM 2	0%	143 d	3/8/04	9/24/04	42 d		
817	Software Support For SEP Integration	0%	115 d	4/15/04	9/24/04	42 d	828SS	
818	Generate SEPT integration & test procedure	0%	5 d	3/8/04	3/12/04	86 d	829FS-10 d	830,843
819	Generate LET integration & test procedure	0%	5 d	4/5/04	4/9/04	67 d	830FS-10 d	831,844
820	Generate HET integration & test procedure	0%	5 d	6/7/04	6/11/04	24 d	832FS-10 d	833,846
821	Generate SIT integration & test procedure	0%	5 d	7/2/04	7/8/04	15 d	837FS-10 d	838,851
822	Generate SEP system test procedure	0%	5 d	7/5/04	7/9/04	15 d	838FS-10 d	839,853
823	Software Support For Acceptance Testing of SEP Instruments	0%	27 d	7/19/04	8/24/04	64 d	839SS	
824	Generate SEP-IDPU integration procedure	0%	5 d	6/8/04	6/14/04	20 d	833FS-10 d	825,834,847
825	Generate SEP-IDPU test procedure	0%	5 d	6/15/04	6/21/04	20 d	824	835,848
826	Software Support of SEP Accelerator Tests	0%	20 d	7/1/04	7/28/04	83 d	873SS-10 d	
827	FM 1	0%	86 d	3/19/04	7/19/04	10 d		
828	Integrate/test SEP Central FM 1	0%	1 d	4/15/04	4/15/04	42 d	409,411,413,69,39	817SS,830
829	Receive & inspect SEPT FM 1	0%	1 d	3/19/04	3/19/04	81 d	605,606	818FS-10 d,830
830	Integrate & test SEPT FM 1	0%	1 d	4/16/04	4/16/04	62 d	818,828,829	819FS-10 d,831
831	Integrate & test LET FM 1	0%	1 d	5/10/04	5/10/04	47 d	819,830,545,547	833
832	Receive & inspect HET FM 1	0%	1 d	6/18/04	6/18/04	19 d	498	820FS-10 d,833
833	Integrate & test HET FM 1	0%	1 d	6/21/04	6/21/04	19 d	820,831,832	824FS-10 d,834,838
834	Integrate SEP FM 1 - IDPU FM 1	0%	1 d	6/22/04	6/22/04	19 d	824,833,162	835
835	Test SEP FM 1 - IDPU FM 1	0%	1 d	6/23/04	6/23/04	19 d	825,834	836
836	Rework SEP FM 1 - IDPU FM 1 issues and retest	0%	5 d	6/24/04	6/30/04	19 d	835	838,356
837	Receive & inspect SIT FM 1	0%	1 d	7/15/04	7/15/04	10 d	676	821FS-10 d,838
838	Integrate & test SIT FM 1	0%	1 d	7/16/04	7/16/04	10 d	821,833,837,836	822FS-10 d,839
839	SEP system test FM 1	0%	1 d	7/19/04	7/19/04	10 d	822,838	823SS,16
840	FM 2	0%	100 d	3/15/04	8/2/04	5 d		

ID	Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
841	Integrate/test SEP Central FM 2	0%	1 d	4/15/04	4/15/04	68 d	410,412,414,391,4	843,852
842	Receive & inspect SEPT FM 2	0%	1 d	3/15/04	3/15/04	91 d	608,609	843
843	Integrate & test SEPT FM 2	0%	1 d	4/16/04	4/16/04	68 d	818,841,842	844
844	Integrate & test LET FM 2	0%	1 d	5/10/04	5/10/04	53 d	819,843,546,547	846
845	Receive & inspect HET FM 2	0%	1 d	7/16/04	7/16/04	5 d	499	846
846	Integrate & test HET FM 2	0%	1 d	7/19/04	7/19/04	5 d	820,844,845	847
847	Integrate SEP FM 2 - IDPU FM 2	0%	1 d	7/20/04	7/20/04	5 d	824,846,163	848
848	Test SEP FM 2 - IDPU FM 2	0%	1 d	7/21/04	7/21/04	5 d	825,847	849
849	Rework SEP FM 2 - IDPU FM 2 issues and retest	0%	5 d	7/22/04	7/28/04	5 d	848	851,362
850	Receive & inspect SIT FM 2	0%	1 d	7/22/04	7/22/04	9 d	679	851
851	Integrate & test SIT FM 2	0%	1 d	7/29/04	7/29/04	5 d	821,849,850	853,852
852	Integrate FM 2 LVPS into SEP Central (if necessary)	0%	1 d	7/30/04	7/30/04	5 d	841,75,851	853
853	SEP FM 2 system test	0%	1 d	8/2/04	8/2/04	5 d	822,851,852	865
854	Environmental Test (at JPL)	0%	119 d	5/3/04	10/19/04	26 d		
855	Generate Environmental Test Procedures	0%	15 d	5/3/04	5/21/04	54 d		856,858
856	Manufacture test cables (JPL)	0%	10 d	5/24/04	6/7/04	54 d	855	858
857	FM 1	0%	41 d	8/6/04	10/4/04	11 d		
858	FM 1 LET/HET/SEP Central Vibration/Acoustics Test	0%	10 d	8/6/04	8/19/04	11 d	16FS+3 d,855,856	866,859,42SS
859	FM 1 LET/HET/SEP Central Thermal Balance Test	0%	5 d	8/20/04	8/26/04	11 d	858	860,43SS
860	FM 1 LET/HET/SEP Central T/V Test	0%	15 d	8/27/04	9/17/04	11 d	859	861,863,44SS
861	Evaluate SEP performance	0%	10 d	9/20/04	10/1/04	36 d	860	862SS
862	Generate test reports	0%	11 d	9/20/04	10/4/04	36 d	861SS	
863	Bakeout Clean SEP exterior & prepare for delivery	0%	3 d	9/20/04	9/22/04	11 d	860	5,45SS
864	FM 2	0%	54 d	8/3/04	10/19/04	5 d		
865	FM 2 EM/EMC Test at CalTech	0%	5 d	8/3/04	8/9/04	5 d	853	866,680,55SS
866	FM 2 LET/HET/SEP Central Vibration/Acoustics Test	0%	10 d	8/20/04	9/2/04	15 d	865,858	867,57SS
867	FM 2 LET/HET/SEP Central Thermal Balance Test	0%	5 d	9/3/04	9/10/04	15 d	866	868,58SS
868	FM 2 LET/HET/SEP Central T/V Test	0%	15 d	9/13/04	10/1/04	15 d	867	869,871,59SS
869	Evaluate SEP performance	0%	10 d	10/4/04	10/18/04	26 d	868	870SS
870	Generate test reports	0%	11 d	10/4/04	10/19/04	26 d	869SS	
871	Bakeout Clean SEP exterior & prepare for delivery	0%	3 d	10/4/04	10/6/04	15 d	868	6,60SS
872	Accelerator Test (at MSU)	0%	10 d	7/15/04	7/28/04	83 d		
873	LET/SEP Cen end-to-end test at Accelerator	0%	10 d	7/15/04	7/28/04	83 d	880	826SS-10 d,46SS
874	GSE	48%	333 d	8/1/03	11/26/04	0 d		
875	Design hardware	0%	71 d	1/5/04	4/14/04	157 d		
876	GSE H/W Calibration equipment	0%	71 d	1/5/04	4/14/04	157 d		
877	End-to-End Test Equipment	86%	234 d	8/11/03	7/14/04	83 d		
878	Design	90%	203 d	8/11/03	6/1/04	83 d		879FS+21 d
879	Assemble	0%	5 d	7/1/04	7/7/04	83 d	878FS+21 d	880
880	Test	0%	5 d	7/8/04	7/14/04	83 d	879	873

ID		Task Name	% Comp	Dur	Start	Finish	TS	Pred	Succ
881		GSE software	31%	333 d	8/1/03	11/26/04	0 d		
882		Write user's guide	0%	4 d	2/2/04	2/5/04	0 d		884
883		Build GSE software release 2	70%	114 d	8/1/03	1/15/04	14 d		884
884		Write user's guide supplement	0%	10 d	2/6/04	2/20/04	0 d	882,883	885FS+65 d
885		Build GSE software release 3	0%	130 d	5/24/04	11/26/04	0 d	884FS+65 d	