

Requesting Engineer: Selda Heavenel

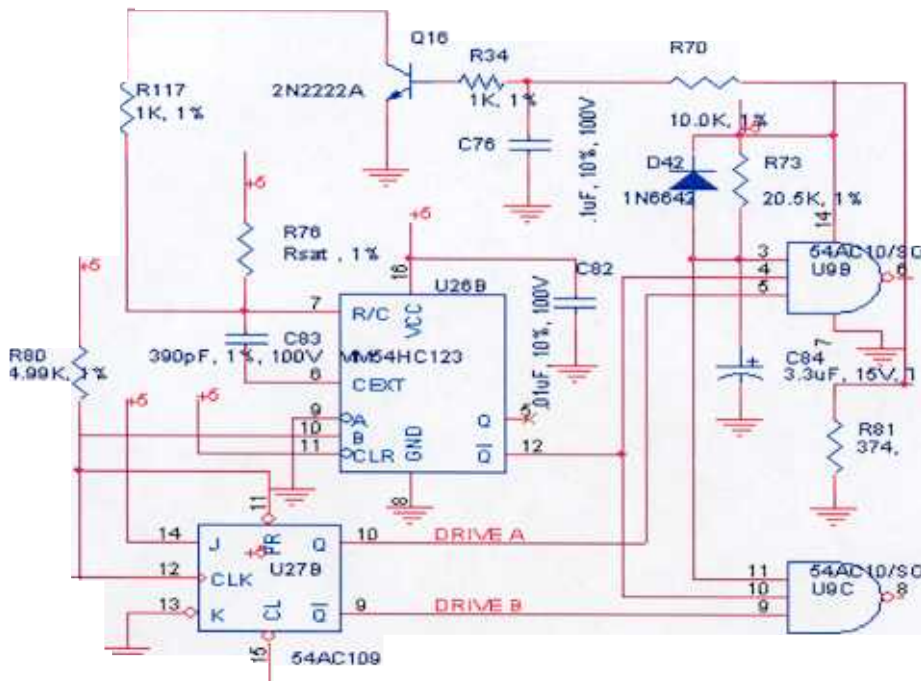
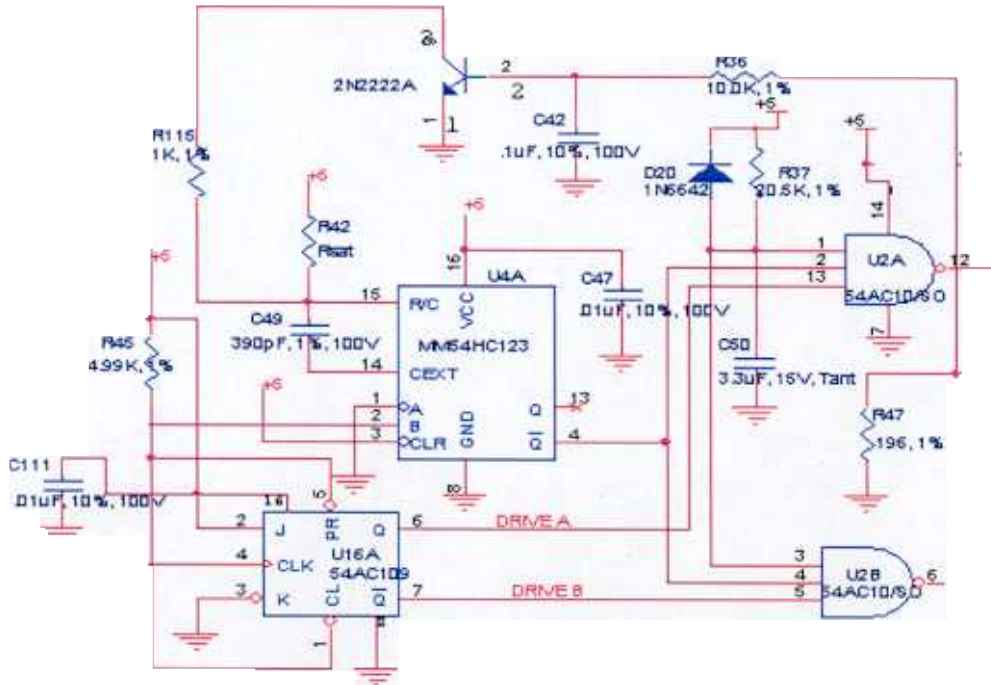
Approved by: Peter King

Date: 04/02/04

Date: 2 April 2004

### STEREO IMPACT SEP TOP LVPS FM1 REWORK INSTRUCTIONS

The SEP TOP board has a short between U16 Pin 6 and U16 Pin 10 due to layout generating software merging separate schematic signals with identical net names. In order to separate the signals, four traces need to be cut.



STEREO IMPACT SEP LVPS TOP BOARD

Requesting Engineer: Selda Heavner

Approved by: Peter King 3/17/2004

Date: 04/02/04

Date: 4-02-04

1) Remove U16.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

2) Cut the trace between U16 pin 6 and U16 pin 10. (See Figure 1)

Completed by \_\_\_\_\_ Date \_\_\_\_\_

3) Cut the trace between U16 pin 7 and U16 pin 9 (See Figure 1)

Completed by \_\_\_\_\_ Date \_\_\_\_\_

4) Cut the trace between U29 Pin 9 to the via (See Figure 2)

Completed by \_\_\_\_\_ Date \_\_\_\_\_

5) Cut the trace between U29 Pin 13 to the via (See Figure 2)

Completed by \_\_\_\_\_ Date \_\_\_\_\_

6) INSPECT the board and test with ohmmeter if the pins are disconnected.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

7) Epoxy cuts at the U16 location. Don't epoxy the U29 cuts until after the jumper wires are attached and inspected in a later step. Use 3M Scotch Weld 1838 green epoxy.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

8) INSPECT.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

9) Install new U16 54AC109 (JM38510R75304SFA) and record traceability information.

Reference Designator: D/C

Completed by \_\_\_\_\_ Date \_\_\_\_\_

**Note:** T13 will be mounted using a stainless steel screw. The jump wires will be twisted and go through the outer mounting hole of T13.

10) Jump U16 Pin 10 to U29 Pin 9 using a #24 AWG wire (Note this wire jump will be twisted with the wire between U16 pin 9 to U29 pin 13). Use shrink tubing over the wires where they go through the holes. Twist the wires per NASA harnessing standard.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

STEREO IMPACT SEP LVPS TOP BOARD

3/17/2004

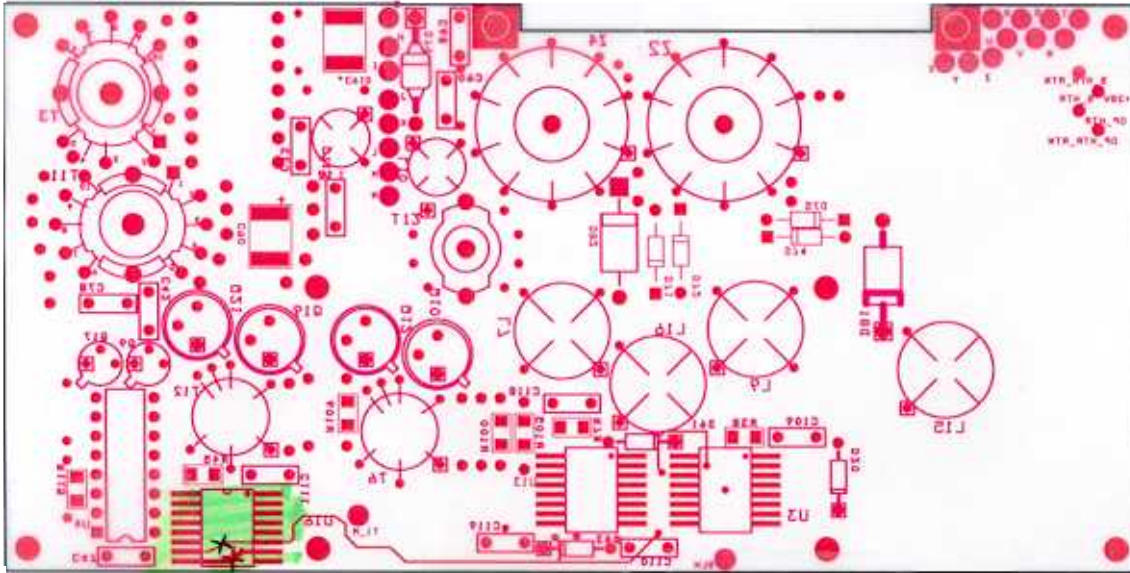
Requesting Engineer: Selda Heavenler

Approved by: Patricia King

Date: 04/02/04

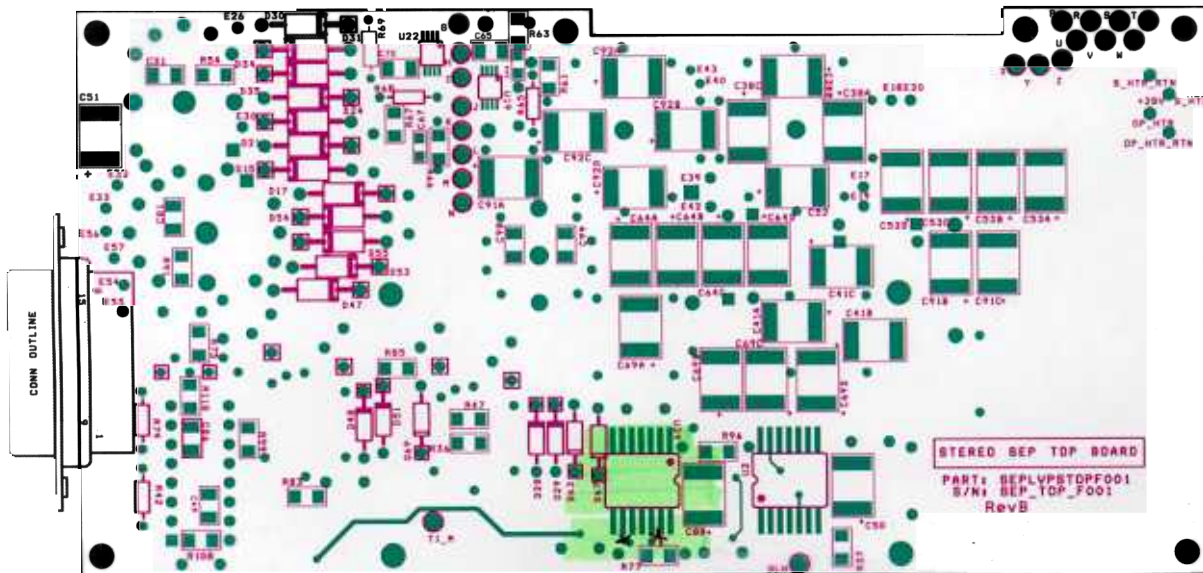
Date: 4-02-04

Figure 1:



BOT SOLDER SIDE - LAYER 6

Figure 2:



Board Layers  
 SST SILK SCREEN TOP  
 TOP COMPONENT SIDE- LAYER 1

STEREO IMPACT SEP LVPS TOP BOARD

Requesting Engineer: Selda Fleavner

Approved by: Peter [Signature] 3/17/2004

Date: 04/02/04

Date: 2 April

11) Jump U16 Pin 9 to U29 Pin 13 using #24AWG wire (Note this wire jump will be twisted with the wire between U16 pin 10 to U29 pin 9). Use shrink tubing over the wires where they go through the holes. Twist the wires per NASA harnessing standard.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

12) INSPECT

Completed by \_\_\_\_\_ Date \_\_\_\_\_

13) Epoxy cuts at U29 location. Use 3M 1838 green epoxy.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

14) INSPECT

Completed by \_\_\_\_\_ Date \_\_\_\_\_

15) Stake jumpers. Use Uralane 5753 for staking.

Completed by \_\_\_\_\_ Date \_\_\_\_\_

Approved by: [Signature]

Date: 2004-4-2

Approved by: Ron Jackson

Date: 4-2-04